

#### Mid Power-3030 series

\$1W0-3030xx7006-00000000-NC001 \$TW7C2\$B-NT

(Cool, Neutral, Warm)



















# **Product Brief**

#### **Description**

- This White Colored surface-mount LED comes in standard package dimension. Package Size: 3.0x3.0x0.66mm
- It has a substrate made up of a molded plastic reflector sitting on top of a lead frame.
- The die is attached within the reflector cavity and the cavity is encapsulated by silicone.
- The package design coupled with careful selection of component materials allow these products to perform with high reliability.

#### **Features and Benefits**

- Thermally Enhanced Package Design
- Mid Power to High Power up to 1.5W
- Max. Driving Current 250mA
- Compact Package Size
- High Color Quality with CRI Min.70
- Pb-free Reflow Soldering Application

#### **Key Applications**

- Replacement lamps Bulb, Tube
- Commercial
- Industrial
- Residential

**Table 1. Product Selection Table** 

Bundant Code	Color	Naminal CCT	Doub Name hou	CRI
Product Code	Color	Nominal CCT	Part Number	Min
		6500K	S1W0-3030657006-000000000-NC001	
	Cool White	5700K	S1W0-3030577006-000000000-NC001	
		5000K	S1W0-3030507006-000000000-NC001	
STW7C2SB-NT-E1H1C100 (DISTY)	Neutral White	4000K	S1W0-3030407006-000000000-NC001	70
		3500K	S1W0-3030357006-000000000-NC001	
	Warm White	3000K	S1W0-3030307006-00000000-NC001	
	•	2700K	S1W0-3030277006-00000000-NC001	



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# **Performance Characteristics**

Table 2. Product Selection Guide,  $I_F = 150mA$ ,  $T_j = 25^{\circ}C$ , RH30%

	CCT (K) [1]	Reference Luminous Flux		Reference Efficacy	PPF	PPE
Part Number	Time	ФV	(lm)	lm/W	μmol/s	μmol/J
	Тур.	Min	Тур	Тур	@150mA	@150mA
	6500	150	160	182	2.14	2.44
	5700	156	162	184	2.09	2.38
	5000	156	165	188	2.15	2.45
S1W0-3030xx70 06-00000000-N C001	4000	156	164	186	2.12	2.42
	3500	150	159	181	2.08	2.37
	3000	144	152	173	2.05	2.33
-	2700	144	149	169	1.98	2.26

#### Notes:

- (1) Correlated Color Temperature is derived from the CIE 1931 Chromaticity diagram.
- (2) The luminous intensity Iv was measured at the peak of the spatial pattern which may not be aligned with the mechanical axis of the LED package.
- (3) The lumen table is only for reference.
- (4) The lumen efficacy table is only for reference.

# **Performance Characteristics**

Table 3. Characteristics, I<sub>F</sub>=150mA, T<sub>i</sub>= 25°C, RH30%

Dozamator	Ch o.l	Value			Unit
Parameter	Symbol	Min.	Тур.	Max. <sup>[4]</sup>	Unit
Forward Current	l <sub>F</sub>	-	150	250	mA
Forward Voltage	V <sub>F</sub>	-	5.85	-	V
CRI	R <sub>a</sub>	70	-	-	Deg.
Viewing Angle	2Θ <sub>1/2</sub>	-	120	-	Deg.
Storage Temperature	$T_{stg}$	- 40	-	+ 100	°C
Thermal resistance (J to S) [3]	Rθ <sub>J-S</sub>	-	11	17	°C/W
ESD Sensitivity(HBM)	-	Class 2 JESD22-A114-E			

**Table 4. Absolute Maximum Ratings** 

Parameter	Symbol	Value	Unit
Forward Current	I <sub>F</sub>	250	mA
Power Dissipation	$P_{D}$	1.5	W
Junction Temperature	T <sub>j</sub>	125	°C
Operating Temperature	T <sub>opr</sub>	-40 ~ + 105	°C
Storage Temperature	T <sub>stg</sub>	-40 ~ + 100	°C

#### Notes:

- [1] Tolerance : VF : $\pm$ 0.1V, Flux : $\pm$ 5%, Ra : $\pm$ 2, x,y : $\pm$ 0.005
- [2]  $2\Theta_{1/2}$  is the off-axis where the luminous intensity is 1/2 of the peak intensity.
- [3] Thermal resistance : Rth<sub>JS</sub> (Junction / solder)
- [4] It is recommended to use it in the condition that the reliability is secured within the Max value.
  - Calculated performance values are for reference only.
- LED's properties might be different from suggested values like above and below tables if
  operation condition will be exceeded our parameter range. Care is to be taken that power
  dissipation does not exceed the absolute maximum rating of the product.
- Thermal resistance can be increased substantially depending on the heat sink design/operating condition, and the maximum possible driving current will decrease accordingly.
- · All measurements were made under the standardized environment of Seoul Semiconductor.

Fig 1. Color Spectrum, T<sub>i</sub> = 25°C, I<sub>F</sub>=150mA

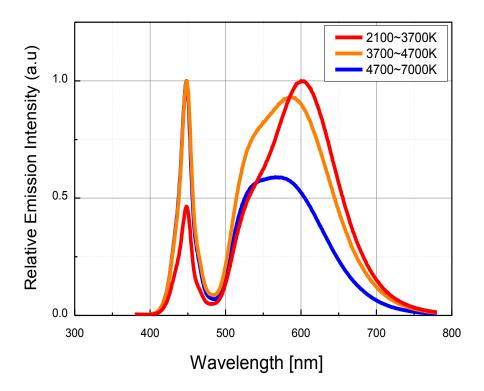


Fig 2. Radiant Pattern, T<sub>i</sub> = 25°C, I<sub>F</sub>=150mA

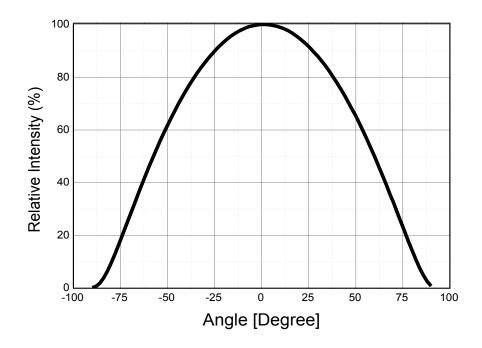


Fig 3. Forward Voltage vs. Forward Current, T<sub>i</sub> = 25°C

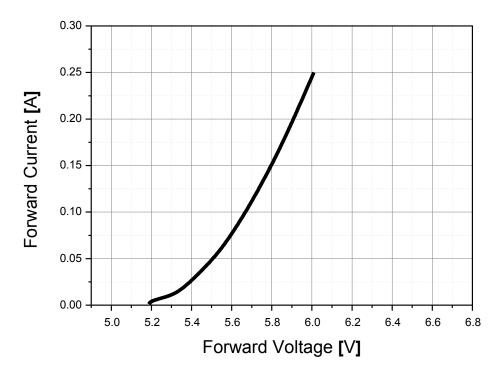


Fig 4. Forward Current vs. Relative Luminous Intensity, T<sub>i</sub> = 25°C

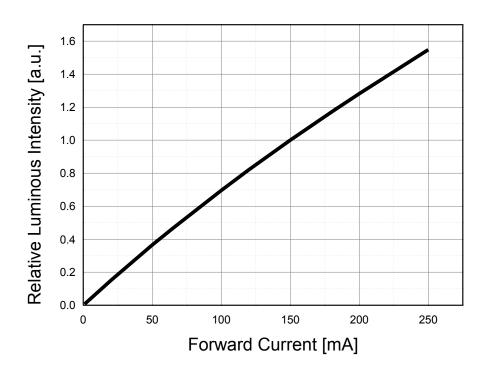
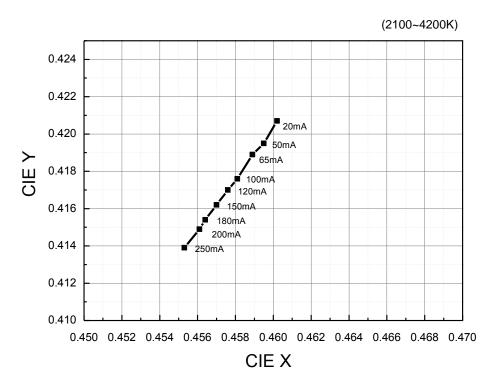


Fig 5. Forward Current vs. CIE X,Y Shift, T<sub>i</sub> = 25°C



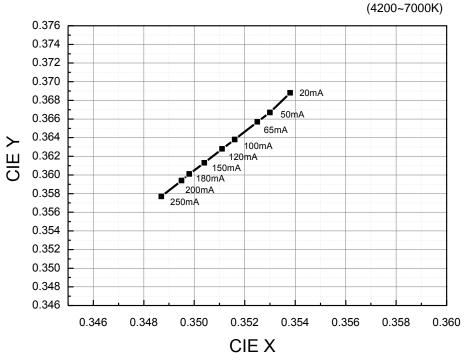


Fig 6. Junction Temperature vs. Relative Luminous Intensity, I<sub>F</sub>=150mA

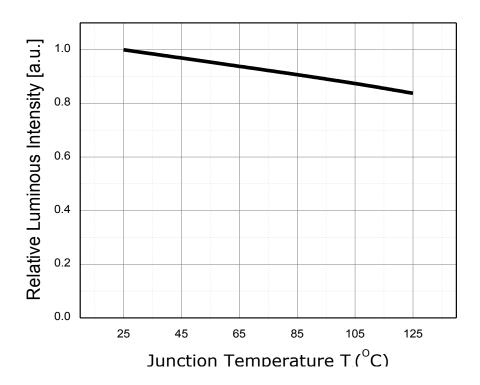


Fig 7. Junction Temperature vs. Relative Forward Voltage, I<sub>F</sub>=150mA

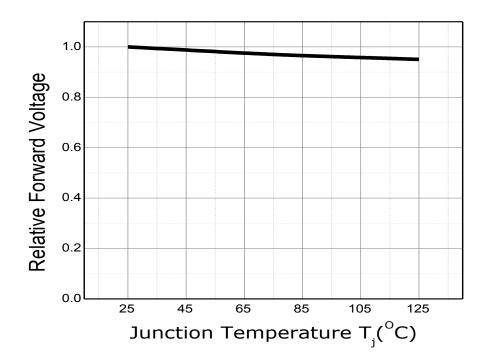
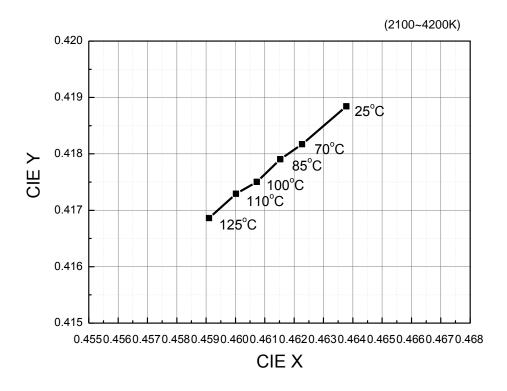


Fig 8. Chromaticity Coordinate vs. Junction Temperature, I<sub>F</sub>=150mA



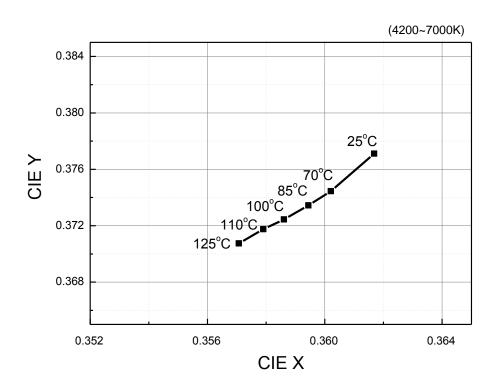
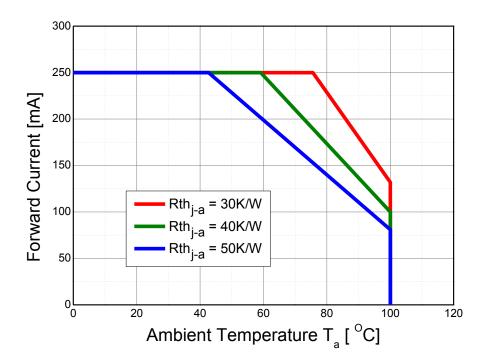


Fig 9. Ambient Temperature vs. Maximum Forward Current,  $T_{j\_max} = 125^{\circ}C$ 





# **Color Bin Structure**

Table 5. Bin Code description, T<sub>i</sub>=25°C, I<sub>F</sub>=150mA

Part Number	Luminous Flux (lm) <sup>[1]</sup>			Color Chromaticity	Typical Forward Voltage (V)		
	Bin Code	Min.	Max.	Coordinate	Bin Code	Min.	Max.
	M48	144	150			5.6	
041410 0000 = 70	N50	150	156	Refer to Page. 12~18	Z56		5.8
S1W0-3030xx70 06-00000000- NC001	N52	156	162				
INCOUT	N54	162	168	- -	<b>Z</b> 58	5.8	6.0
	N56	168	174	'			

#### Table 6. Intensity rank distribution

Available ranks

ССТ	CIE	Flux Rank						
6000 ~ 7000K	А	M48	N50	N52	N54	N56		
5300- 6000K	В	M48	N50	N52	N54	N56		
4700 ~ 5300K	С	M48	N50	N52	N54	N56		
3700 ~ 4200K	E	M48	N50	N52	N54	N56		
3200 ~ 3700K	F	M48	N50	N52	N54	N56		
2900 ~ 3200K	G	M48	N50	N52	N54	N56		
2600 ~ 2900K	Н	M48	N50	N52	N54	N56		

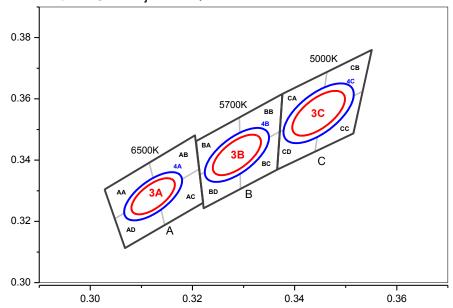
<sup>[1]</sup> Further consultation is required to ensure availability.

#### \*Notes:

- (1) Calculated performance values are for reference only.
- All measurements were made under the standardized environment of Seoul Semiconductor.
   In order to ensure availability, single color rank will not be orderable.

# **Color Bin Structure**

# CIE Chromaticity Diagram T<sub>i</sub>=25°C, I<sub>F</sub>=150mA



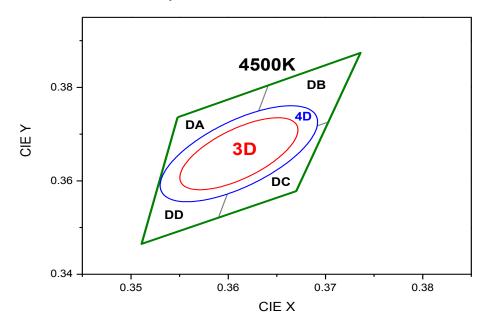
6500	K 3Step	5700	K 3Step	5000K 3Step		
	3A		3B	3C		
Center point	0.3123 : 0.3282	Center point	0.3287 : 0.3417	Center point	0.3447 : 0.3553	
Major Axis a	0.00669	Major Axis a	0.00746	Major Axis a	0.00822	
Minor Axis b	0.00285	Minor Axis b	0.00320	Minor Axis b	0.00354	
Ellipse	58.57	Ellipse	59.09	Ellipse	59.62	
Rotation Angle	56.57	Rotation Angle	39.09	Rotation Angle	39.02	

6500	K 4Step	5700	K 4Step	5000K 4Step		
	4A		4B	4C		
Center point	0.3123 : 0.3282	Center point	0.3287 : 0.3417	Center point	0.3447 : 0.3553	
Major Axis a	0.00892	Major Axis a	0.00995	Major Axis a	0.01096	
Minor Axis b	0.00380	Minor Axis b	0.00427	Minor Axis b	0.00472	
Ellipse	58.57	Ellipse	Ellipse 59.09		59.62	
Rotation Angle	30.37	Rotation Angle	33.03	Rotation Angle	33.02	

	Α	Α	Α	В	Α	С	Α	D
	CIE X	CIE Y						
	0.3028	0.3304	0.3115	0.3393	0.3131	0.329	0.3048	0.3209
	0.3048	0.3209	0.3131	0.329	0.3146	0.3187	0.3068	0.3113
	0.3131	0.329	0.3213	0.3371	0.3221	0.3261	0.3146	0.3187
	0.3115	0.3393	0.3205	0.3481	0.3213	0.3371	0.3131	0.329
ВА		В	В	В	C	В	D	
	CIE X	CIE Y						
	0.3207	0.3462	0.3292	0.3539	0.3293	0.3423	0.3215	0.3353
	0.3215	0.3353	0.3293	0.3423	0.3294	0.3306	0.3222	0.3243
	0.3293	0.3423	0.3371	0.3493	0.3366	0.3369	0.3294	0.3306
	0.3292	0.3539	0.3376	0.3616	0.3371	0.3493	0.3293	0.3423
	С	A	C	В	C	C	C	D
	CIE X	CIE Y						
	0.3376	0.3616	0.3463	0.3687	0.3452	0.3558	0.3371	0.3493
	0.3371	0.3493	0.3452	0.3558	0.344	0.3428	0.3366	0.3369
	0.3452	0.3558	0.3533	0.3624	0.3514	0.3487	0.344	0.3428
	0.3463	0.3687	0.3551	0.376	0.3533	0.3624	0.3452	0.3558

# **Color Bin Structure**

# CIE Chromaticity Diagram T<sub>i</sub>=25°C, I<sub>F</sub>=150mA



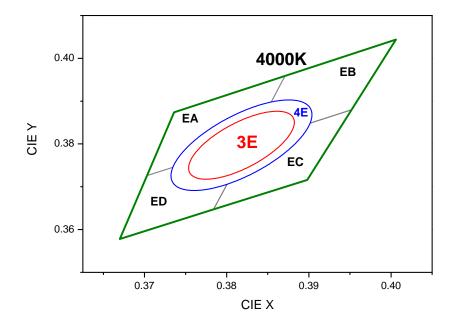
# 4500K 3Step 3D Center point 0.3611, 0.3658 Major Axis a 0.009 Minor Axis b 0.0039 Ellipse 55 Rotation Angle

4500K 4Step								
4D								
Center point	0.3611, 0.3658							
Major Axis a	0.012							
Minor Axis b	0.0052							
Ellipse	55							
Rotation Angle	55							

DA		DB		DC		DD	
CIE X	CIE Y						
0.3548	0.3736	0.3641	0.3804	0.3616	0.3663	0.3530	0.3601
0.3530	0.3601	0.3616	0.3663	0.3590	0.3521	0.3511	0.3465
0.3616	0.3663	0.3703	0.3726	0.3670	0.3578	0.3590	0.3521
0.3641	0.3804	0.3736	0.3874	0.3703	0.3726	0.3616	0.3663

# **Color Bin Structure**

# CIE Chromaticity Diagram $T_j=25$ °C, $I_F=150$ mA



#### 4000K 3Step 3E

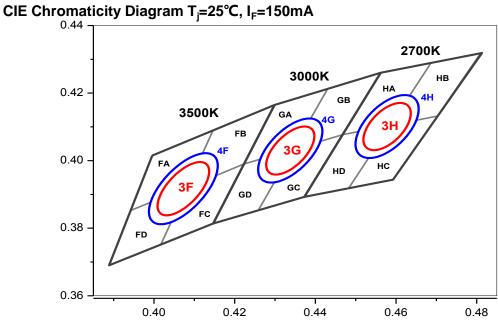
	3E		
Center point	0.3818 : 0.3797		
Major Axis a	0.00939		
Minor Axis b	0.00402		
Ellipse	53.72		
Rotation Angle	33.72		

#### 4000K 4Step

	4E	
Center point	0.3818 : 0.3797	
Major Axis a	0.01252	
Minor Axis b	0.00536	
Ellipse	53.72	
Rotation Angle	55.72	

E	Α	Е	В	E	С	Е	D
CIE X	CIE Y						
0.3736	0.3874	0.3871	0.3959	0.3828	0.3803	0.3703	0.3726
0.3703	0.3726	0.3828	0.3803	0.3784	0.3647	0.367	0.3578
0.3828	0.3803	0.3952	0.388	0.3898	0.3716	0.3784	0.3647
0.3871	0.3959	0.4006	0.4044	0.3952	0.388	0.3828	0.3803

# **Color Bin Structure**



3500K 3Step		3000K 3Step		2700K 3Step	
3 Step		3 Step		3 Step	
Center point	0.4073 : 0.3917	Center point	0.4338 : 0.4030	Center point	0.4578 : 0.4101
Major Axis a	0.00927	Major Axis a	0.00834	Major Axis a	0.00810
Minor Axis b	0.00414	Minor Axis b	0.00408	Minor Axis b	0.00420
Ellipse	54.00	Ellipse	53.22	Ellipse	53.70
Rotation Angle	J <del>4</del> .00	Rotation Angle	33.22	Rotation Angle	55.70

	3500K 4Step		3000K 4Step		2700K 4Step		
4 Step		4 Step		4 Step			
	Center point	0.4073 : 0.3917	Center point	0.4338 : 0.4030	Center point	0.4578 : 0.4101	
	Major Axis a	0.01236	Major Axis a	0.01112	Major Axis a	0.01080	
	Minor Axis b	0.00552	Minor Axis b	0.00544	Minor Axis b	0.00560	
	Ellipse	54.00	Ellipse	53.22	Ellipse	53.70	
	<b>Rotation Angle</b>	54.00	Rotation Angle		Rotation Angle	55.70	

F.	A	F	В	F	С	F	D
CIE X	CIE Y						
0.3996	0.4015	0.4146	0.4089	0.4082	0.392	0.3943	0.3853
0.3943	0.3853	0.4082	0.392	0.4017	0.3751	0.3889	0.369
0.4082	0.392	0.4223	0.399	0.4147	0.3814	0.4017	0.3751
0.4146	0.4089	0.4299	0.4165	0.4223	0.399	0.4082	0.392
G	A	G	В	G	С	G	D
CIE X	CIE Y						
0.4299	0.4165	0.443	0.4212	0.4345	0.4033	0.4223	0.399
0.4223	0.399	0.4345	0.4033	0.4259	0.3853	0.4147	0.3814
0.4345	0.4033	0.4468	0.4077	0.4373	0.3893	0.4259	0.3853
0.443	0.4212	0.4562	0.426	0.4468	0.4077	0.4345	0.4033
Н	A	н	В	H	С	Н	D
CIE X	CIE Y						
0.4562	0.426	0.4687	0.4289	0.4585	0.4104	0.4468	0.4077
0.4468	0.4077	0.4585	0.4104	0.4483	0.3919	0.4373	0.3893
0.4585	0.4104	0.4703	0.4132	0.4593	0.3944	0.4483	0.3919
0.4687	0.4289	0.481	0.4319	0.4703	0.4132	0.4585	0.4104

# **Mechanical Dimensions**

# Top View Bottom View

# Side View Cathode Anode

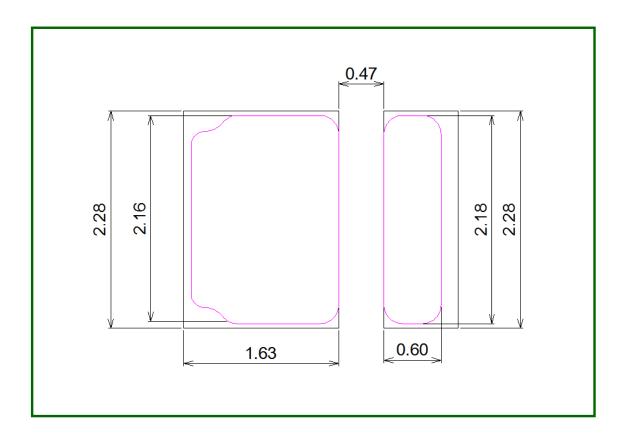
#### Notes:

(1) All dimensions are in millimeters.

(2) Scale: none

(3) Undefined tolerance is  $\pm 0.2 mm$ 

# **Recommended Solder Pad**



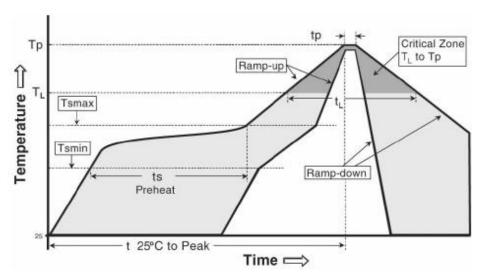
Notes limensions are in millimeters.

(2) Scale: none

(3) This drawing without tolerances are for reference only

(4) Undefined tolerance is  $\pm 0.1 \text{mm}$ 

# **Reflow Soldering Characteristics**



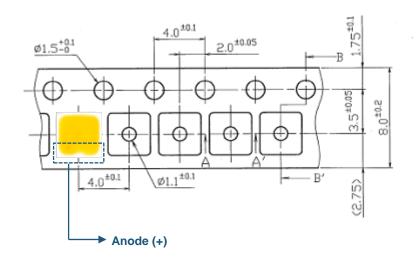
**IPC/JEDEC J-STD-020** 

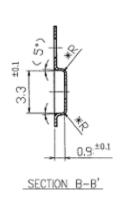
Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate $(T_{s\_max} \text{ to } T_p)$	3° C/second max.	3° C/second max.
$ \begin{array}{c} \text{Preheat} \\ \text{- Temperature Min } (T_{s\_min}) \\ \text{- Temperature Max } (T_{s\_max}) \\ \text{- Time } (T_{s\_min} \text{ to } T_{s\_max}) (t_s) \end{array} $	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-180 seconds
Time maintained above: - Temperature (T <sub>L</sub> ) - Time (t <sub>L</sub> )	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak Temperature (T <sub>p</sub> )	215℃	260℃
Time within 5°C of actual Peak Temperature (t <sub>p</sub> )2	10-30 seconds	20-40 seconds
Ramp-down Rate	6 °C/second max.	6 °C/second max.
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

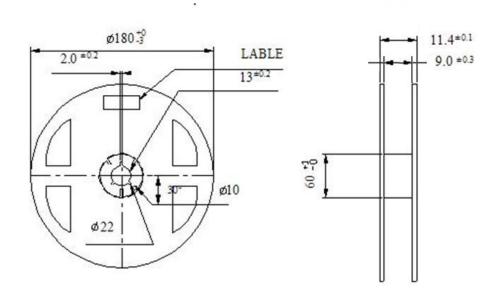
#### Caution:

- (1) Reflow soldering is recommended not to be done more than two times
  In the case of more than 24 hours passed soldering after first, LEDs will be damaged.
- (2) Repairs should not be done after the LEDs have been soldered When repair is unavoidable, suitable tools must be used.
- (3) Die slug is to be soldered.
- (4) When soldering, do not put stress on the LEDs during heating.
- (5) After soldering, do not warp the circuit board.

# **Emitter Tape & Reel Packaging**







( Tolerance:  $\pm 0.2$ , Unit: mm )

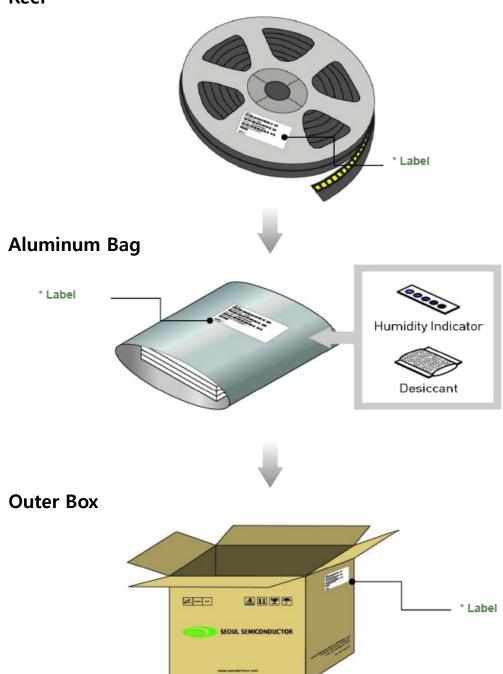
#### Notes:

- (1) Quantity: Max 4,500pcs/Reel
- (2) Cumulative Tolerance : Cumulative Tolerance/10 pitches to be  $\pm 0.2$ mm
- (3) Adhesion Strength of Cover Tape

  Adhesion strength to be 0.1-0.7N when the cover tape is turned off from the carrier tape at the angle of 10° to the carrier tape.
- (4) Package : P/N, Manufacturing data Code No. and Quantity to be indicated on a damp proof Package.

# **Emitter Tape & Reel Packaging**

# Reel



# **Product Nomenclature**

#### **Part Numbering System**

Part Number Code	Description	Part Number	Value
<b>X</b> <sub>1</sub>	Company	S	Seoul Semiconductor
X <sub>2</sub>	Level of Integration	1	Discrete LED
X <sub>3</sub> X <sub>4</sub>	Technology	Wo	General White
$X_5X_6X_7X_8$	Dimension	3030	
X <sub>9</sub> X <sub>10</sub>	ССТ	xx	
X <sub>11</sub> X <sub>12</sub>	CRI	70	
X <sub>13</sub> X <sub>14</sub>	Vf	06	
X <sub>15</sub> X <sub>16</sub> X <sub>17</sub>	Characteristic code Flux Rank	000	
X <sub>18</sub> X <sub>19</sub> X <sub>20</sub>	Characteristic code Vf Rank	000	
X <sub>21</sub> X <sub>22</sub>	Characteristic code Color Step		
X <sub>23</sub> X <sub>24</sub>	Туре	NC	
X <sub>25</sub> X <sub>26</sub> X <sub>27</sub>	Internal code	001	

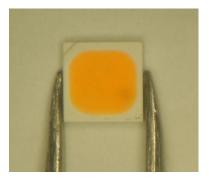


# **Handling of Silicone Resin for LEDs**

(1) During processing, mechanical stress on the surface should be minimized as much as possible. Sharp objects of all types should not be used to pierce the sealing compound.



(2) In general, LEDs should only be handled from the side. By the way, this also applies to LEDs without a silicone sealant, since the surface can also become scratched.



- (3) When populating boards in SMT production, there are basically no restrictions regarding the form of the pick and place nozzle, except that mechanical pressure on the surface of the resin must be prevented. This is assured by choosing a pick and place nozzle which is larger than the LED's reflector area.
- (4) Silicone differs from materials conventionally used for the manufacturing of LEDs. These conditions must be considered during the handling of such devices. Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust.

As mentioned previously, the increased sensitivity to dust requires special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of components.

- (5) SSC suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin.

  Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the LED.
- (6) Please do not mold this product into another resin (epoxy, urethane, etc) and do not handle this. product with acid or sulfur material in sealed space.



# **Precaution for Use**

(1) Storage

To avoid the moisture penetration, we recommend store in a dry box with a desiccant. The recommended storage temperature range is 5°C to 30°C and a maximum humidity of RH50%.

(2) Use Precaution after Opening the Packaging

Use SMT techniques properly when you solder the LED as separation of the lens may affect the light output efficiency.

Pay attention to the following:

- a. Recommend conditions after opening the package
  - Sealing / Temperature : 5 ~ 40°C Humidity : less than RH30%
- b. If the package has been opened more than 4 week(MSL\_2a) or the color of the desiccant changes, components should be dried for 10-12hr at 60±5°C
- (3) Do not apply mechanical force or excess vibration during the cooling process to normal temperature after soldering.
- (4) Do not rapidly cool device after soldering.
- (5) Components should not be mounted on warped (non coplanar) portion of PCB.
- (6) Radioactive exposure is not considered for the products listed here in.
- (7) Gallium arsenide is used in some of the products listed in this publication. These products are dangerous if they are burned or shredded in the process of disposal. It is also dangerous to drink the liquid or inhale the gas generated by such products when chemically disposed of.
- (8) This device should not be used in any type of fluid such as water, oil, organic solvent and etc. When washing is required, IPA (Isopropyl Alcohol) should be used.
- (9) When the LEDs are in operation the maximum current should be decided after measuring the package temperature.

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- (10) The appearance and specifications of the product may be modified for improvement without notice.
- (11) Long time exposure of sunlight or occasional UV exposure will cause lens discoloration.
- (12) VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate silicone encapsulants of LEDs and discolor when exposed to heat and photonic energy. The result can be a significant loss of light output from the fixture. Knowledge of the properties of the materials selected to be used in the construction of fixtures can help prevent these issues.
- (13) Attaching LEDs, do not use adhesives that outgas organic vapor.
- (14) The driving circuit must be designed to allow forward voltage only when it is ON or OFF.
  If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.
- (15) Similar to most Solid state devices;
  LEDs are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS).
  Below is a list of suggestions that Seoul Semiconductor purposes to minimize these effects.
- a. ESD (Electro Static Discharge)

Electrostatic discharge (ESD) is the defined as the release of static electricity when two objects come into contact. While most ESD events are considered harmless, it can be an expensive problem in many industrial environments during production and storage. The damage from ESD to an LEDs may cause the product to demonstrate unusual characteristics such as:

- Increase in reverse leakage current lowered turn-on voltage
- Abnormal emissions from the LED at low current

The following recommendations are suggested to help minimize the potential for an ESD event. One or more recommended work area suggestions:

- Ionizing fan setup
- ESD table/shelf mat made of conductive materials
- ESD safe storage containers

One or more personnel suggestion options:

- Antistatic wrist-strap
- Antistatic material shoes
- Antistatic clothes

#### Environmental controls:

- Humidity control (ESD gets worse in a dry environment)



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#### b. EOS (Electrical Over Stress)

Electrical Over-Stress (EOS) is defined as damage that may occur when an electronic device is subjected to a current or voltage that is beyond the maximum specification limits of the device. The effects from an EOS event can be noticed through product performance like:

- Changes to the performance of the LED package
  (If the damage is around the bond pad area and since the package is completely encapsulated the package may turn on but flicker show severe performance degradation.)
- Changes to the light output of the luminaire from component failure
- Components on the board not operating at determined drive power

Failure of performance from entire fixture due to changes in circuit voltage and current across total circuit causing trickle down failures. It is impossible to predict the failure mode of every LED exposed to electrical overstress as the failure modes have been investigated to vary, but there are some common signs that will indicate an EOS event has occurred:

- Damaged may be noticed to the bond wires (appearing similar to a blown fuse)
- Damage to the bond pads located on the emission surface of the LED package (shadowing can be noticed around the bond pads while viewing through a microscope)
- Anomalies noticed in the encapsulation and phosphor around the bond wires.
- This damage usually appears due to the thermal stress produced during the EOS event.
- c. To help minimize the damage from an EOS event Seoul Semiconductor recommends utilizing:
  - A surge protection circuit
  - An appropriately rated over voltage protection device
  - A current limiting device



# **Company Information**

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#### **Company Information**

Seoul Semiconductor (www.SeoulSemicon.com) manufacturers and packages a wide selection of light emitting diodes (LEDs) for the automotive, general illumination/lighting, Home appliance, signage and back lighting markets. The company is the world's fifth largest LED supplier, holding more than 10,000 patents globally, while offering a wide range of LED technology and production capacity in areas such as "nPola", "Acrich", the world's first commercially produced AC LED, and "Acrich MJT - Multi-Junction Technology" a proprietary family of high-voltage LEDs.

The company's broad product portfolio includes a wide array of package and device choices such as Acrich and Acirch2, high-brightness LEDs, mid-power LEDs, side-view LEDs, and through-hole type LEDs as well as custom modules, displays, and sensors.

#### **Legal Disclaimer**

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