

ACLAVIS 7.0"

Master Data Sheet



Compliant with RoHS directive 2011/65/EU

Description:

LCD: 17.8 cm [7.0"] TFT Display

Resolution: 800 x 480 pixel Cover Glass: 2 mm glass

Dimension: 193.5 x 132.5 mm (cover lens)

Max. Thickness: 14.5 mm (complete unit)

Available types (part numbers):

GETM0700G0EDH6GLYN01 = chemically strengthened, anti-glare

GETM0700G0EDH6GLYN02 = chemically strengthened, anti-reflective, anti-finger print

GETM0700G0EDH6GLYN03 = standard float glass, glossy



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Appendix:

 Technical Drawings (cover glass and complete unit). The drawings show the dimensions of the Anti-Glare version **GETM0700G0EDH6GLYN01**, which is representative for all types.



Your Benefit

Cover Glass Design

- Corners are rounded (R4)
- Edges are smoothened and chamfered
- Double silk-screen printing for increased opacity (deep black RAL 9005)



Surface Treatment (Anti-Glare, Anti-Reflection, Anti-Finger Print)

Anti-glare (etching) used for **GETM0700G0EDH6GLYN01**.

The smooth surface is ideal for touch application.

- ➤ Anti-smudge effect
- > Easy to clean
- Pleasant feel for fingers
- Diffused light-reflection

Anti-Glare is no coating but an etching of the surface. Therefore, it is not susceptible to abrasion.

Anti-Reflection and Anti-Finger Print coating used for the **GETM0700G0BDH6GLYN02.**

In contrast to the anti-glare treatment this coating does NOT diffuse light but reduces reflections. An additional anti-finger print coating makes it robust and easy to clean. Regarding abrasion see Schott's technical data sheet (CONTURAN® DARO). **GETM0700G0BDH6GLYN03** has no surface treatment.



Chemically strengthened

➤ Up to 5 times higher bending and breaking strength

Used for GETM0700G0EDH6GLYN01 and GETM0700G0EDH6GLYN02.

Optical Bonding

The cover lens and LCD unit optically bonded (full-faced). This provides exceptionally high adhesion.

- No humidity or foreign particles can get in between
- ➤ Best possible touch sensor performance
- > Reduction of light reflection
- Increased stability

True Multi-Touch

Thanks to EDT's PolyTouch technology, this product offers **simultaneous 5-finger** recognition.

The integrated controller offers a **standard I²C interface**. Sensitivity of the touch sensor can easily be changed via standard register addresses.

V 1.1 04.05.2016



GLYN TFT Family Concept

This 7" LCD offers the **same interface** as our standard GLYN family 3.5", 4.3", 5.0" and 5.7" products (without cover glass). The main features of this family are:

- > -20 to 70°C (operating)
- > Integrated voltage generation for LED backlight and TFT driving
- ➤ 3.3V single power supply
- One connector (touch and backlight integrated)
- > PWM dimming control (backlight brightness)



1.0. General Specification

Weight: ~400 g

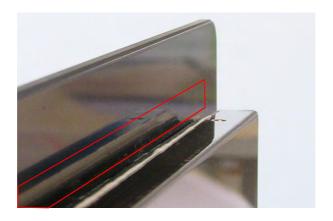
Brightness: ca. 10% less compared to LCD without cover glass

Color Tolerance: $\Delta E \le 1.0$ (this applies to the silk-screen print of the cover glass)

2.0. Inspection and Handling Standards

2.1. Bonding Glue

Due to the "Bonding" process slight amounts of glue may leak beyond the bonding area. Glue residue may be visible on the glass print up to 5 mm outside the touch sensor area.





2.2. Use of Cleaning and Adhesive Products

The **front side** has no special coating (except for the anti-reflection version which needs to be handled with slightly more care). Therefore, it is **not** particularly sensitive to cleaning agents. Standard glass cleaning products may be used. In rare cases, deionised (demineralized) water in combination with high temperature can cause a reaction. This chemical reaction may cause stains to be visible on the surface.

The **rear side** is covered with a silk-screen print. This print is susceptible to mechanical impact and abrasion. Aggressive cleaning agents should **not** be used. For more information on the paint please take a look at the Technical Data (appendix of this document).

The usability of any glue or cleaning agent (front side, rear side) has to be tested and approved by the customer.

2.3. Cosmetic Quality Inspection Criteria

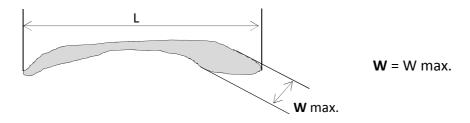
Our cosmetic quality inspection is aimed at insuring high quality. The following components are prone to contain potential defects:

- Display glass panel (surface)
- Display polarization film
- Air gap between polarizer and touch sensor
- Touch sensor bonding (inside touch sensor)
- Touch sensor glass (surface)
- Optical bonding between touch sensor and cover glass
- Cover glass (including printing)

Glass is a raw material which <u>always</u> contains micro-impurities. Most of them cannot be seen with the naked eye. The following specification defines the maximum allowed number and size of scratches, digs and foreign matter. Non-specified impurities/defects or <u>a combination of a few minor ones in the cover glass</u> need to be handled <u>on an individual basis</u>. For more information please contact our **QM department**.

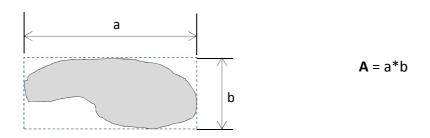


2.3.1. Scratches (scratch-like impurities) and foreign material (e.g. long fibers)



Length: L (in mm)	Width: W (in mm)	Allowed number			
L ≤ 0.3	W ≤ 0.05	Ignore			
0.3 < L ≤ 2.5	0.05 < W ≤ 0.1	4			
2.5 < L ≤ 4.0	0.05 < W ≤ 0.1	1			
> 4.0	> 0.1	Not allowed			
The total number must not exceed 4 (e.g. 1 x up to 4.0 mm plus 3 x up to 2.5 mm)					

2.3.2. Digs, bubbles, stains, dents, pinholes (in printing), foreign material (e.g. near round-shaped or rectangular-shaped particles)



Area = (a*b) (in mm)	Allowed number
A ≤ 0.1	Ignore
0.1 < A ≤ 0.36	4
> 0.36	Not allowed



- a) Long defects (2.3.1.) and spots/digs (2.3.2.) are regarded as cumulative. 2 x long defects and 2 x spots/digs at the same time are acceptable.
- b) A concentration of defects is not allowed
- c) Removable stains or marks are not regarded as defects
- d) Edge chips: $5 \times 0.4 \text{ mm}$ (e.g. size = $0.4 \times 0.4 \text{ mm}$) are acceptable
- e) Defects like clouds/stains (mura) exist in a great variety and are sometimes observed only under special conditions (light, angle, reflection etc.). Therefore, they are not specified because these effects have to be discussed and specified based on bad/good limit samples. Clearly visible clouds/stains (mura) which can immediately be seen under all conditions (see below specified time, distance and angle) are not allowed.

Condition of Inspection

Inspection in transmission, reflection, printed glasses, always from the viewer's side.

Inspection angle 45° to 90° Inspection time: max. 10 sec.

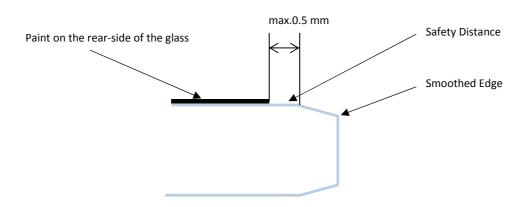
Distance: 500 mm

Max. 1000 Lux in front of black background Colour temperature: D65 (CIE standard)



2.4. Silk-screen print

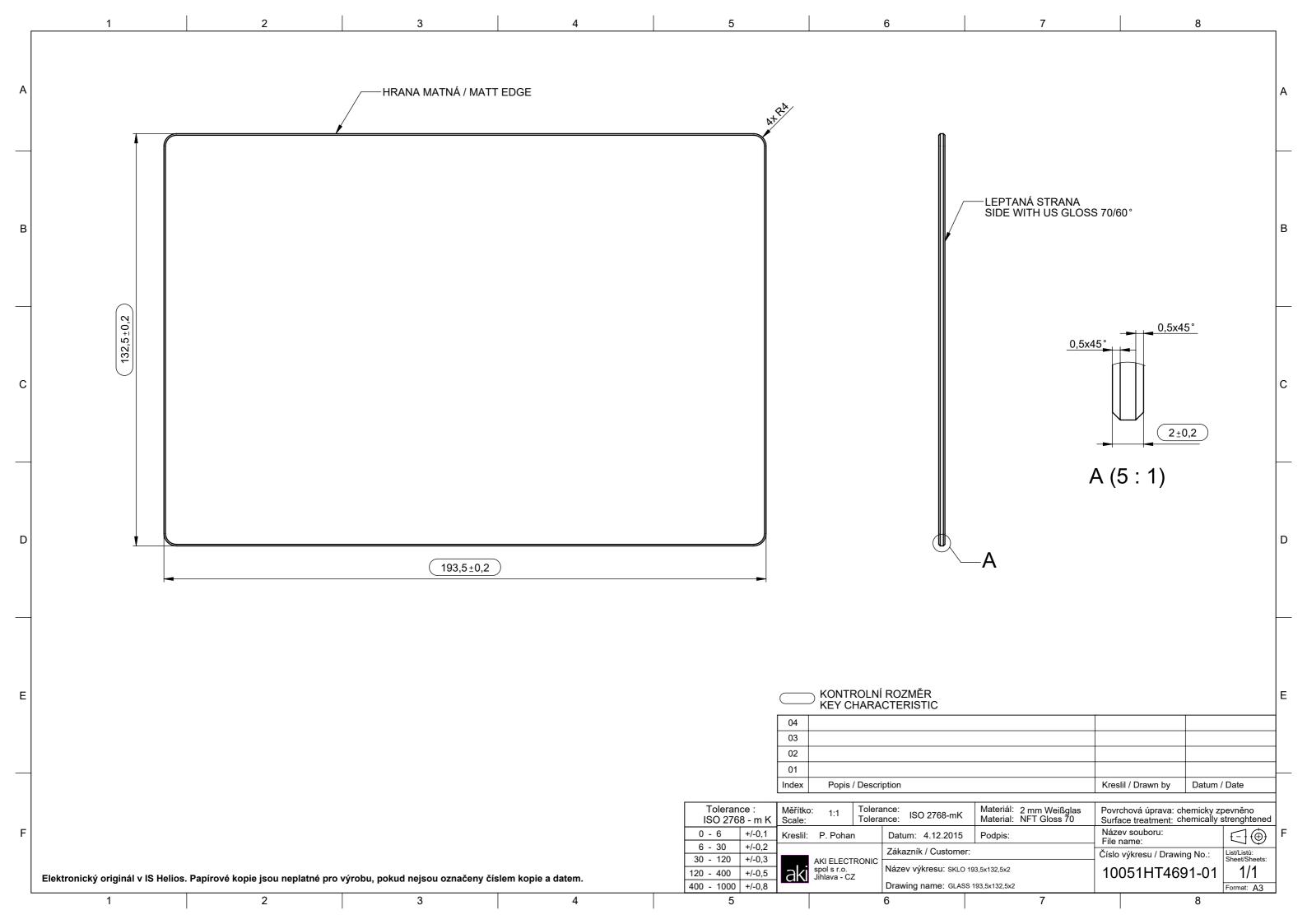
The printing process is set up in a way that there is no paint overflow at the edge. Therefore, a safety distance has to be kept to the edge. This may lead to a small area without paint.

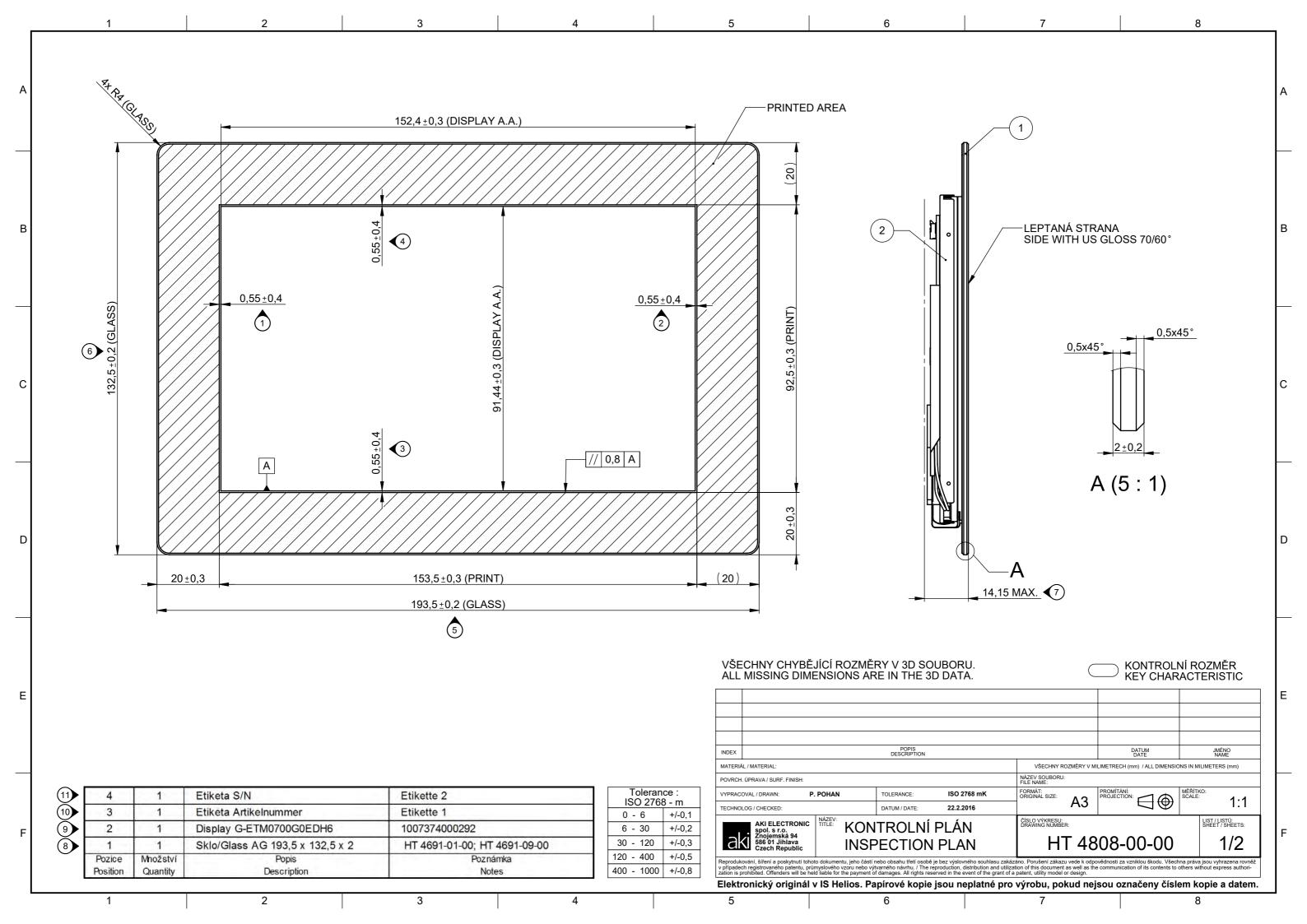


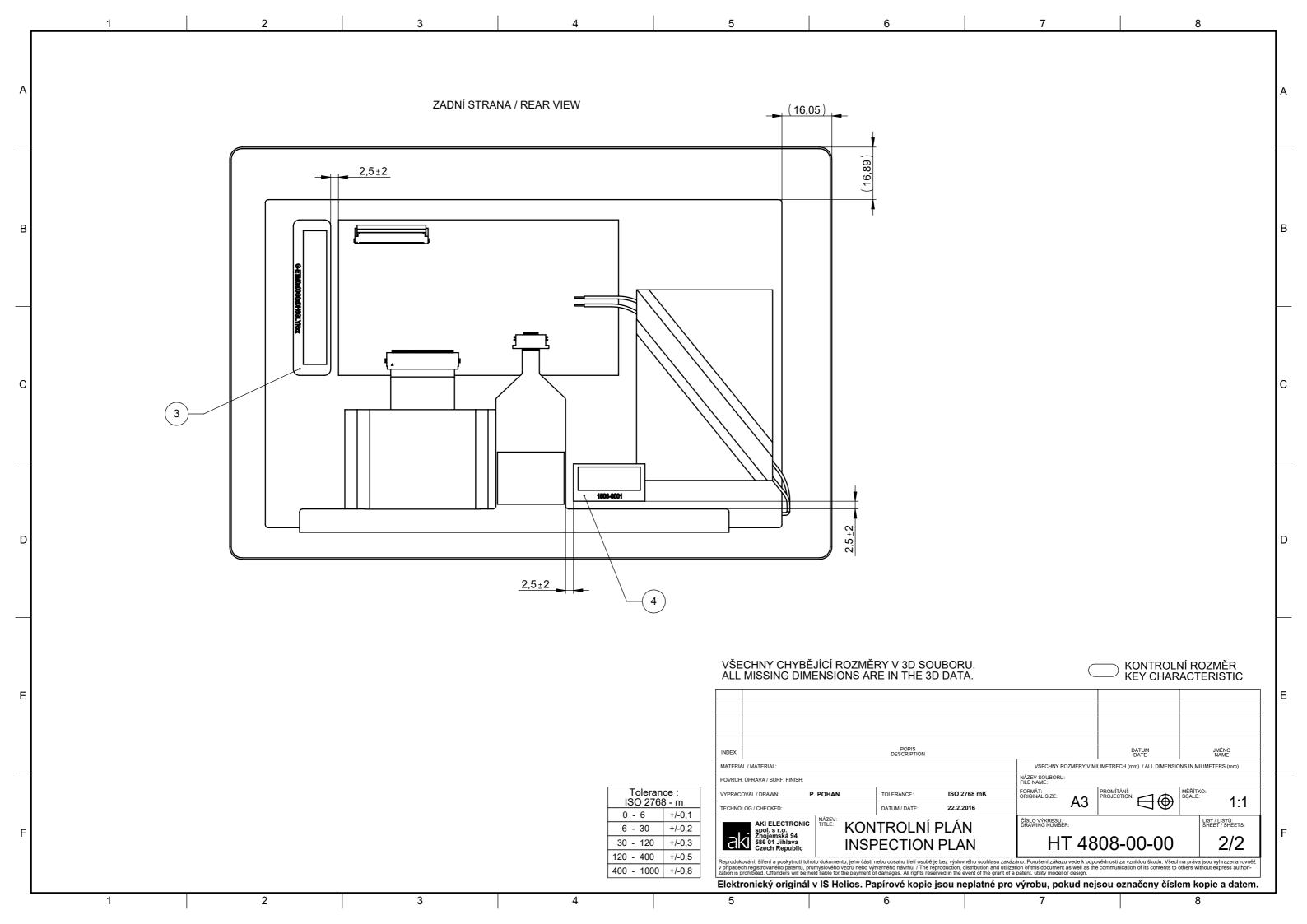
3.0. Revision History

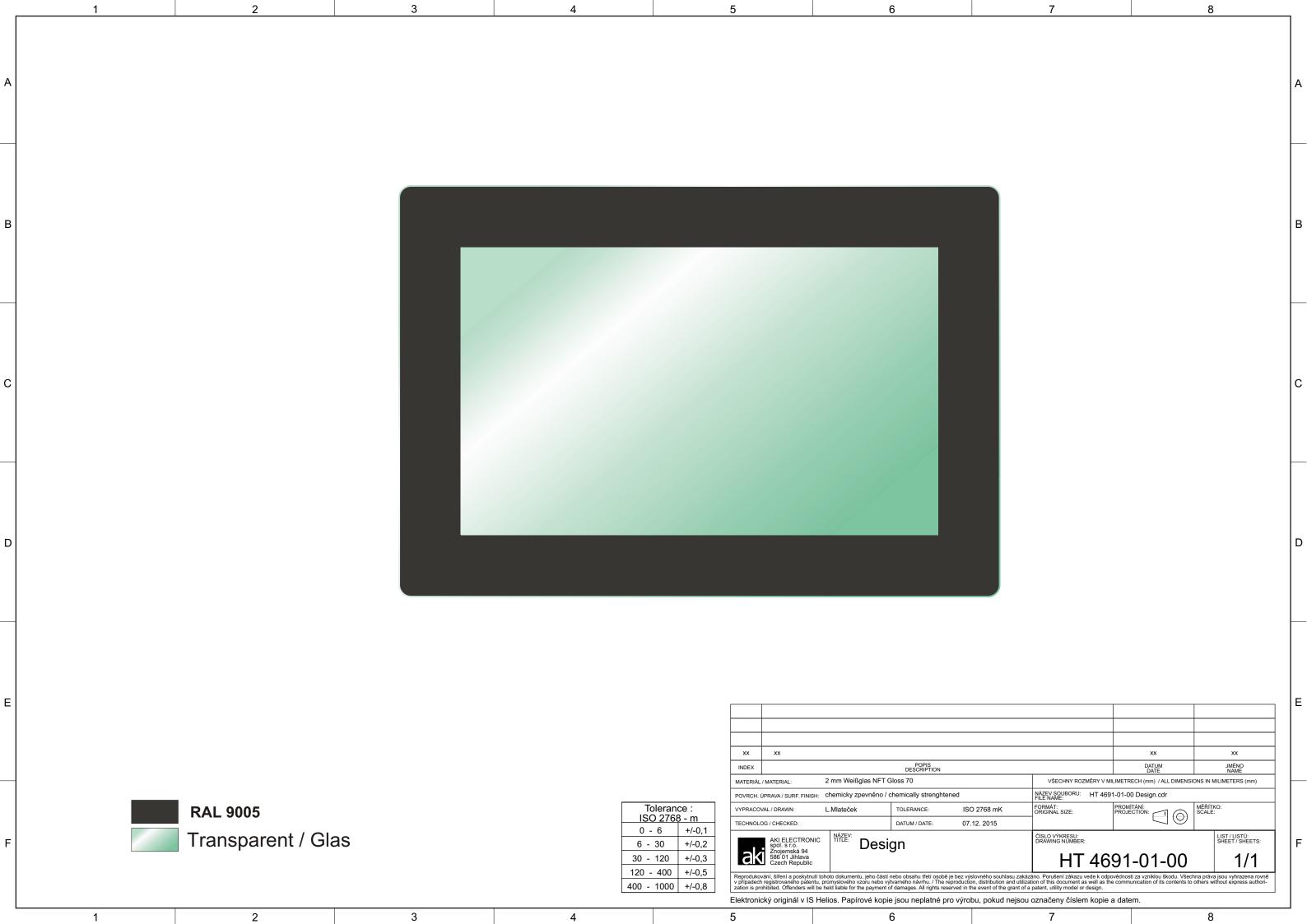
Date	Version	Section	Remark
15.03.2016	1.0	-	First release
04.05.2016	1.1	-	Two versions added with AR coating (GLYN02) and without treatment (GLYN03).

The information contained herein is subject to change without prior notice. This master data sheet does not replace the original information of the LCD data sheet. For valid specification please refer to the original manufacturer's data sheet.









Durable Anti-Fingerprint Coating on Anti-Reflective Cover Glass

DARO* coating minimizing fingerprints – the breakthrough for any professional touch application – now available on CONTURAN®

SCHOTT has developed a new coating combination with durable anti-reflective, anti-fingerprint, easy to clean and anti-smudge (oleo- & hydrophobic) characteristics, a breakthrough for any professional touch application.

Anti-reflective coatings are significantly enhancing the contrast of today's high resolution displays, but their usage in touch applications suffers from the issue of fingerprint visibility on the surface of such low-reflective glass.

The availability of a super tough and super durable antifingerprint coating, along with an anti-reflective coating has been requested for a long time and is now being offered by SCHOTT on its well known material CONTURAN®, now available as CONTURAN® DARO.

SCHOTT is well equipped to deliver cut-to-size materials with complex edge-processing, screen-printing (organic and ceramic colors are available) and tempering or chemical hardening. We're looking forward to support your products with our expertise.



^{*}DARO stands for Durable Anti-Reflective Oleophobic



Applications

- Touch-screen displays
- Ruggedized displays
- Command and control displays
- Medical displays
- GPS displays (e.g. automotive)
- Kiosks/Public displays
- Gaming displays

Advantages

- Anti-reflective
- Anti-fingerprint
- Durable
- Easy-to-clean
- Water repellant
- Great haptics

Long-term Durability

In order to use an AR coating in a touch application it must have an oleophobic coating that can withstand hundreds of thousands of finger touches and rubs. CONTURAN® DARO fulfills this demand and maintains its properties such as reduced visibility of fingerprints, easy-to-clean properties and good haptics even if subjected to harsh mechanical and chemical stress.

Proof of Durability for Touch Applications

- Inherently resistant to salt spray environments; more than 90 days
- Rub resistance greater than 400,000 rubs per MIL-C-675C (equals > 20 years lifetime based on 10 cleaning cycles per day with 5 rubs/cycle)



Surfaces of touch-screens need to withstand hundreds of thousands of finger touches. Touching the surface leads to mechanical abrasion and to chemical wear because of salt in the sweat on our fingers. In order to prove the durability of CONTURAN® DARO we perform mechanical abrasive tests (cheesecloth rubs) as well as chemical resistance tests (salt spray):

Contact angle is stable for more than 400,000 rubs (contact angle > 90°)

Contact angle is stable for more than 400,000 rubs (contact angle > 90°)

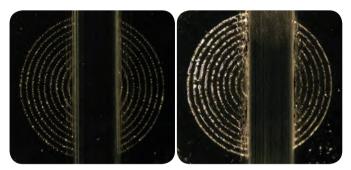
Cleaning 50 times per day → 20 years lifetime!

Number of Dry Cheesecloth Rubs

CONTURAN® DARO — Popular tablet computer



In addition SCHOTT's R&D has developed a method to characterize the fingerprint adhesion on untreated and treated glass sheets. With this test procedure it is possible to get reproducible and quantitative results of the amount of fingerprint transfer onto defined substrates.



Equal applied imprint on CONTURAN® DARO respectively AR-Glass (both glasses wiped dry 3 times)

Specifications (Dimensions & product range) Glass thickness from 1.1 mm up to 6.0 mm Available in sizes up to 990 mm x 1,770 mm (72" diagonal) CONTURAN® Standard CONTURAN® Magic CONTURAN® Grey CONTURAN® BOROFLOAT® Can be combined with all necessary processing as screen printing, tempering, laminating etc.

ABREX® test:

A combination of mechanical and chemical wear is simulated by the ABREX® test commonly used in the automotive industry, tested using artificial sweat. CONTURAN® DARO slightly surpasses the performance of painted plastic parts in automotive industry (e. g. for gear selectors) according to DIN EN 60068-2-70/IEC 68-2-70. CONTURAN® DARO can be used on the gear selector in your car and the oleophobic coating will stand the lifetime of your vehicle.

Advanced Optics
SCHOTT AG
Hattenbergstrasse 10
55122 Mainz
Germany
Phone +49 (0)6131/66-1812
Fax +49 (0)3641/2888-9047
info.optics@schott.com

sergstrasse 10 55122 Mainz Germany 131/66-1812 1/2888-9047 SCHOTT glass made of ideas

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MODEL NO. VERSION PAGE EMERGING DISPLAY TECHNOLOGIES CORPORATION ETM0700G0EDH6 3 0 - 1DOC . FIRST ISSUE DEC.16, 2015 RECORDS OF REVISION REVISED SUMMARY DATE PAGE NO. JAN.18, 2016 14 10.1 IF1: PIN 26~28: ADD DE MODE: HSYNC, VSYNC CONNECTED TO VDD. SYNC MODE: ENB CONNECTED TO GROUND. 7 5.4 CAPACITIVE TOUCH PANEL I2C INTERFACE TIMING APR.06, 2016 **CHARACTERISTICS** SCL FREQUENCY : MIN.=0→-6.1 OPTICAL CHARACTERISTICS 9 (70) (80) (70) (80) NOTE (2 NOTE (3 VIEWING ANGLE $\theta^{\lambda}=0$ CONTRAST RATIO θy=0° NOTE(3) θx=0°, WHITE COLOR OF RED θx=0°, θy=0° IF = 160 mA NTSC : 72 % NOTE (5) COORDINATE GREEN BLUE THE BRIGHTNESS OF MODULE CONDITION MIN. TYP. MAX. UNIT REMARK NOTE (2 NOTE (3 VIEWING ANGLE CONTRAST RATIO NOTE(3) RED COLOR OF NOTE (5) COORDINATE GREEN 0.070 0.120 THE BRIGHTNESS OF cd/m² NOTE (6) 10.1 IF1 14 PIN NO. SYMBOL 25 /WAKE EXTERNAL INTERRUPT FROM THE HOST TOUCH PANEL Sold distill

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TECHNOLOGIES CORPORATION	ETM0700G0EDH6	3	1
1. GENERAL SPECIFICATIONS			
1.1 DATA SHEETS FOR LCD MODUL PLEASE REFER TO :	LE CONTROLLER/DRIVER		
HIMA HIMA			
1.2 DATA SHEET FOR CAPACITIVE	TOUCH PANEL CONTROLLER/D	RIVER	
PLEASE REFER TO: HYC	CON HY4614	4	
1.3 MATERIAL SAFETY DESCRIPTIO ASSEMBLIES SHALL COMPLY W INCLUDING PROHIBITED MATER MERCURY, CADMIUM, HEXAVA BIPHENYLS (PBB) AND POLYBRO DIPHENYL ETHERS (PBDE)	ITH EUROPEAN ROHS REQUIRE RIALS/COMPONENTS CONTAIN LENT CHROMIUM, POLYBROM	NG LEAD,	>°
2. MECHANICAL SPECIFICATIONS	.00	10,	
2.1 LCD MODULE MECHANICAL SPE	ECIFICATIONS	1.00	
(1) DIAGONALS	7 inch	3	
(2) NUMBER OF DOTS	800W * (RGB) * 480H	I DOTS	
(3) MODULE SIZE	166W * 105.44H *11.	6D (MAX.) n	ım
	(WITHOUT FPC & L	ED BL'S CAE	BLE)
(4) EFFECTIVE AREA	154.4W * 93.44H mm		
(5) ACTIVE AREA	152.4W * 91.44H mm		
(6) DOT SIZE	0.0635W * 0.1905H n	nm	
(7) PIXEL	0.1905W * 0.1905H m	m	
SIZE (8) LCD TYPE	TFT , TRANSMISSIV	E ANTI-GL	ARE
(9) COLOR		E,711111 GE	ince
(10) VIEWING DIRECTION		LEVEL INVE	ERSION)
(11) BACK LIGHT			,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
(12) INTERFACE MODE			
(12) INTERIMED	(DE/SYNC MODE)	LL	
2.2 CAPACITIVE TOUCH PANEL MEG			
(1) TOUCH PANEL SIZE	7.0 inch		
(2) OUTER DIMENSION		mm	
(2) 00 150 510101011	(WITHOUT FPC)		
(3) EFFECTIVE AREA	· · · · · · · · · · · · · · · · · · ·		
(4) ACTIVE AREA	154W * 93.05H mm		
(5) INPUT TYPE	MULTI TOUCH		
(6) NUMBER OF TOUCH SENSOR	28 * 16 SENSORS		
(7) RESOLUTION			
(a) DIFFERENCE MODE	12.0		

(8) INTERFACE MODE ------ I2C

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3. ABSOLUTE MAXIMUM RATINGS

3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	-0.3	3.6	V	
INPUT VOLTAGE	VIN-VSS	-0.3	VDD+0.3	V	
STATIC ELECTRICITY	_	_	_	V	NOTE (1)
LED BACKLIGHT POWER DISSIPATION	PD	_	2592	mW	
LED BACKLIGHT FORWARD CURRENT	IF		240	mA	

NOTE (1): LCM SHOULD BE GROUNDED DURING HANDLING LCM.

3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	OPERATING		STORAGE		REMARK	
HEM	MIN. MAX.		MIN. MAX.			
AMBIENT TEMPERATURE	-20°C	70°C	-30°C	80°C	NOTE (1), (2)	
HUMIDITY	NOTE (3 Y/Y NOTE (3 Y 1		WITHOUT CONDENSATION			
VIBRATION		2.45 m/s ² (0.25 G)	01000		5~20Hz, 1HR 20~500Hz(20Hz), 1HR 20~500Hz(500Hz), 1HR X,Y,Z TOTAL 3HRS	
SHOCK		29.4 m/s ² (3 G)		490.0 m/s ² (50 G)	10ms XYZ DIRECTIONS 1 TIME EACH	
CORROSIVE GAS	NOT ACC	EPTABLE	NOT ACC	ÉPTABLE		

NOTE (1): Ta AT -30°C: WILL BE 48HRS MAX.

80°C: WILL BE 168HRS MAX.

NOTE (2): BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE

THIS PHENOMENON IS REVERSIBLE.

NOTE (3): $Ta \le 60^{\circ}C : 90\%RH MAX (96HRS MAX)$.

Ta > 60°C: ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY

OF 90%RH AT 60°C(96HRS MAX).

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4. ELECTRICAL CHARACTERISTICS

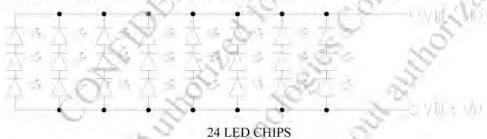
Ta = 25 °C

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS		3.2	3.3	3.6	V	
POWER SUPPLY VOLTAGE FOR LED DRIVER	VCC-VSS		3.0	3.3	3.6	V	
HIGH LEVEL INPUT VOLTAGE	VIH	-	0.7*VDD	Ψ.	VDD	V	NOTE(1)
LOW LEVEL INPUT VOLTAGE	VIL		0	\leftrightarrow	0.3*VDD	V	NOTE (1)
POWER SUPPLY CURRENT	IDD	VDD-VSS=3.3V		240	300	mA	NOTE(2)
POWER SUPPLY CURRENT FOR LED DRIVER	ICC	VCC-VSS=3.3V LED B/L=ON	-	600	780	mA	
POWER SUPPLY FOR LED BACKLIGHT	V _{BL+} -V _{BL} .	IF=160mA	8.4	9.9	10.8	V	NOTE(3)
LED LIFE TIME	T	X (C)	30K	40K	- C-	HRS	A -

NOTE (1): APPLIED TO TERMINALS B5~B0, G5~G0, R5~R0, DCLK, HSYNC, VSYNC, ENB.

NOTE (2): THE DISPLAY PATTERN IS ALL "WHITE"...

NOTE (3): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT



NOTE (4): AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT. (PER. LED)

Allowable Forward Current

Tumung Allowable Forward Current

Allowable Forward Current

Tumung Allowable Forward Current

Output

Outp

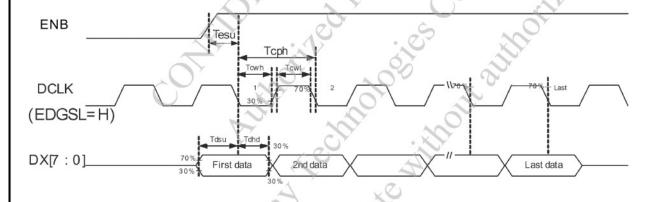
Ambient Temperature vs.

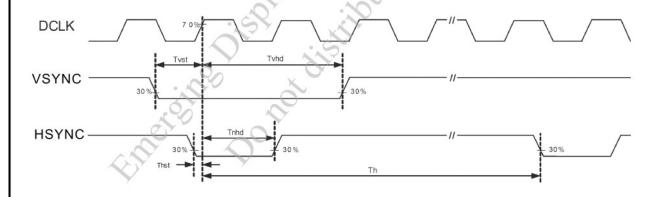
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5. TIMING CHARACTERISTICS

5.1 LCD MODULE AC ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
HSYNC SETUP TIME	Thst	6	_	_	ns
HSYNC HOLD TIME	Thhd	6	- 3	- 2	ns
VSYNC SETUP TIME	Tvst	6		_	ns
VSYNC HOLD TIME	Tvhd	6			ns
DATA SETUP TIME	Tdsu	6	— ×	-0	ns
DATA HOLD TIME	Tdhd	6			ns
ENB SETUP TIME	Tesu	6	_	10	ns
SOURCE OUTPUT SETTLING TIME	T_{ST}	_	- 3	15	μs
SOURCE OUTPUT LOADING R	$R_{ m SL}$	_	2		K ohm
SOURCE OUTPUT LOADING C	C_{SL}	.—.:	60	_ ×	pF

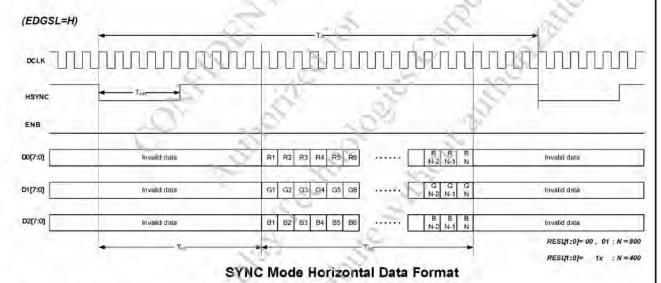


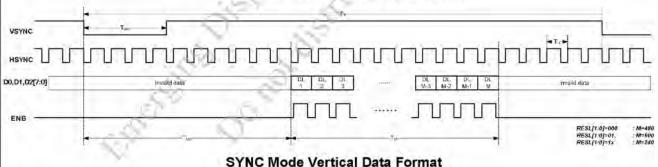


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5.2 LCD MODULE SYNC MODE SIGNAL CHARACTERISTICS

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
DCLK FREQUENCY	F _{CPH}	-	33.26	7=-	MHz
DCLK PERIOD	T_{CPH}		30.06	-	ns
DCLK PULSE DUTY	T_{CWH}	40	50	60	%
HSYNC PERIOD	T_{H}	930	1056	1057	T_{CPH}
HSYNC PULSE WIDTH	T_{WH}	1	128	-	T_{CPH}
HSYNC -FIRST HORIZONTAL DATA TIME	T _{HS}	-	216		T_{CPH}
HSYNC ACTIVE TIME	THA	-	800	-	TCPII
VSYNC PERIOD	$T_{\mathbf{V}}$		525	-	$T_{\rm H}$
VSYNC PULSE WIDTH	T_{WV}	1	2		$T_{\rm H}$
VSYNC -DE TIME	T_{VS}	-	35	Y.=_	TH
VSYNC ACTIVE TIME	T _{VA}	-	480		$T_{\rm H}$



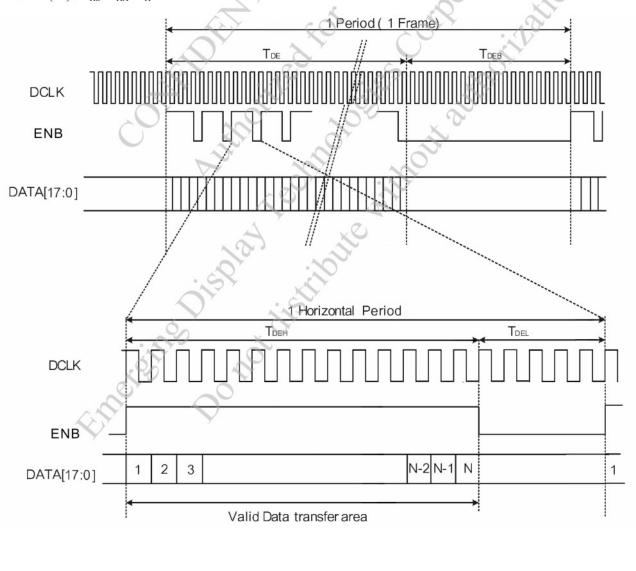


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5.3 LCD MODULE DE MODE SIGNAL CHARACTERISTICS

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
DCLK FREQUENCY	F _{CPH}	_	33.26		MHz
DCLK PERIOD	T_{CPH}	_	30.06		ns
DCLK PULSE DUTY	T_{CWH}	40	50	60	%
ENB PERIOD	$T_{DEH} + T_{DEL}$	1000	1056	1200	T_{CPH}
ENB PULSE WIDTH	T_{DEH}	_	800	_	T_{CPH}
ENB FRAME BLANKING	T_{DEB}	10	45	110	$T_{DEH} + T_{DEL}$
ENB FRAME WIDTH	T_{DE}	_	480	_	$T_{DEH} + T_{DEL}$
OEV PULSE WIDTH	T _{OEV}		150		T_{CPH}
CKV PULSE WIDTH	T_{CKV}	_	133	- (T_{CPH}
ENB(INTERNAL)-STV TIME	T_1	_	4	_ \	T_{CPH}
ENB(INTERNAL)-CKV TIME	T ₂	_	40	-0	T_{CPH}
ENB(INTERNAL)-OEV TIME	T ₃	_	23	. 0	T_{CPH}
ENB(INTERNAL)-POL TIME	T ₄		157	102	T_{CPH}
STV PULSE WIDTH		_	1 4	0"	$T_{\rm H}$

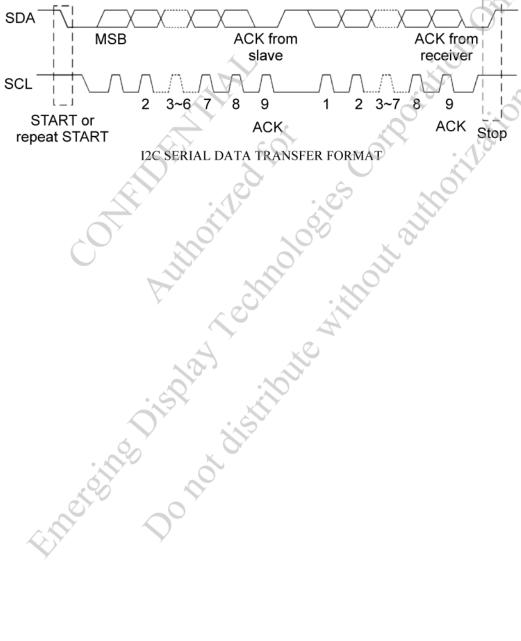
NOTE (1) : $T_{HS}+T_{HA} < T_{H}$



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5.4 CAPACITIVE TOUCH PANEL I2C INTERFACE TIMING CHARACTERISTICS

ITEM	MIN.	TYP.	MAX.	UNIT
SCL FREQUENCY		_	400	KHz
BUS FREE TIME BETWEEN A STOP AND START CONDITION	1.3	_	_	us
HOLD TIME (REPEATED) START CONDITION	0.6	_	_	us
DATA SETUP TIME	100	_	_	ns
SETUP TIME FOR A REPEATED START CONDITION	0.6			us
SETUP TIME FOR STOP CONDITION	0.6	_	_ ^	us

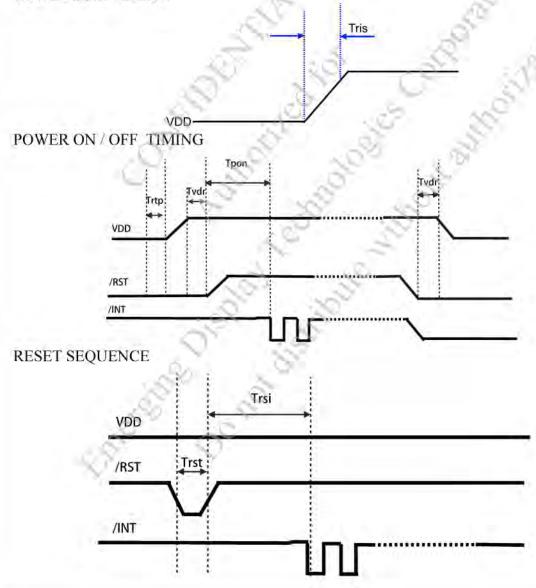


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5.5 CAPACITIVE TOUCH PANEL POWER SEQUENCE

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
RISE TIME FROM 0.1VDD TO 0.9VDD	Tris	7-	-	5	ms
TIME OF RESETTING TO BE LOW BEFORE POWERING ON	Trtp	100			us
TIME OF STARTING TO REPORT POINT AFTER POWERING ON	Tpon	200			ms
RESET TIME AFTER VDD POWERING ON	Tvdr	1	-		ms
TIME OF STARTING TO REPORT POINT AFTER RESETTING	Trsi	200		30	ms
RESET TIME	Trst	2			ms

POWER RISE TIMING



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6. OPTICAL CHARACTERISTICS (NOTE 1)

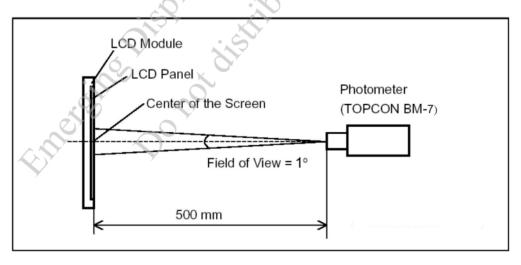
6.1 OPTICAL CHARACTERISTICS

 $Ta = 25 \pm 2$ °C

ITEM		CVMDOL	COMP	ITION	MINI	TVD	MAV	LIMIT	DEMARK		
HEM		SYMBOL	COND	IIION	MIN.	TYP.	MAX.	UNIT	REMARK		
				θ_{y^+}		θ _x =0°	65	70	_		
VIEWING ANGL	VIEWING ANGLE		CR ≥ 10	O _x -O	70	75		deg.	NOTE (2)		
VIEWING ANGL	E	θ_{x^+}	CK ≥ 10	0 -00	70	75		deg.	NOTE (3)		
		θ_{x}		$\theta^{\lambda}=0$	70	75	_		4 .		
CONTRAST RAT	Oľ	CR	θx=0°,	θy=0°	675	900		_ ^	NOTE(3)		
DECDONCE TIME	7	T _R (rise)	000	000	_	5	10		NOTE (4)		
RESPONSE TIME	2	T _F (fall)	$\theta x = 0^{\circ}$,	θy=0°	_	15	20	msec	NOTE (4)		
	WHITE	Wx	4		0.255	0.305	.0355				
	WHITE	Wy			0.275	0.325	0.375	P -			
GOV OR OF	DED	Rx			0.585	0.635	0.685		C.		
COLOR OF	RED	Ry		$\theta x=0^{\circ}$, $\theta y=0^{\circ}$ IF = 160 mA	0.295	0.345	0.395]	NOTE (5)		
CIE COORDINATE	CDEEN	Gx		50 mA : 72 %	0.265	0.315	0.365		NOTE (5)		
COORDINATE	GREEN	Gy	11150	Wise.	0.595	0.645	0.695				
	DLUE	Bx			0.115	0.165	0.215				
	BLUE	By	15		0.020	0.070	0.120	P' —			
THE BRIGHTNESS OF		P	8		200	2.52		1/ 2			
MODULE		В	θx=0°.	θy=0°	308	352	0	cd/m ²	NOTE (C)		
THE UNIFORMITY OF MODULE		-3		60mA	75	80	_	%	NOTE (6)		

NOTE (1): TEST EQUIPMENT SETUP:

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 1° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.

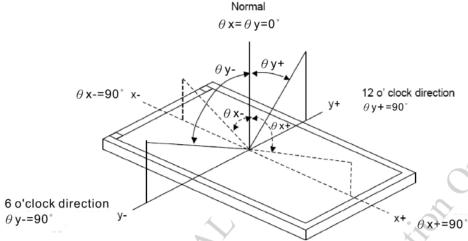


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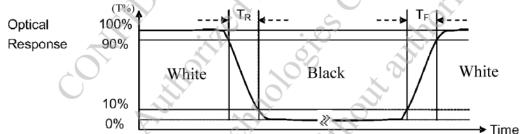
NOTE (2): DEFINITION OF VIEWING ANGLE:



NOTE (3): DEFINITION OF CONTRAST RATIO:

BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE" CONTRAST RATIO(CR) = BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"

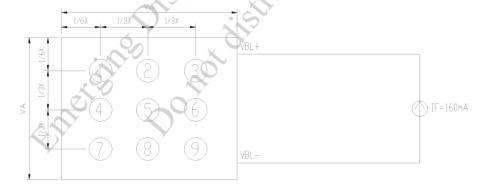
NOTE (4): DEFINITION OF RESPONSE TIME: T_R AND T_F THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



NOTE (5): THE 100% TRANSMISSION IS DEFINED AS THE TRANSMISSION OF LCD PANEL WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY OPENED.

NOTE (6): BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"

6.2 THE TEST METHOD OF BRIGHTNESS AND UNIFORMITY



UNIT: mm

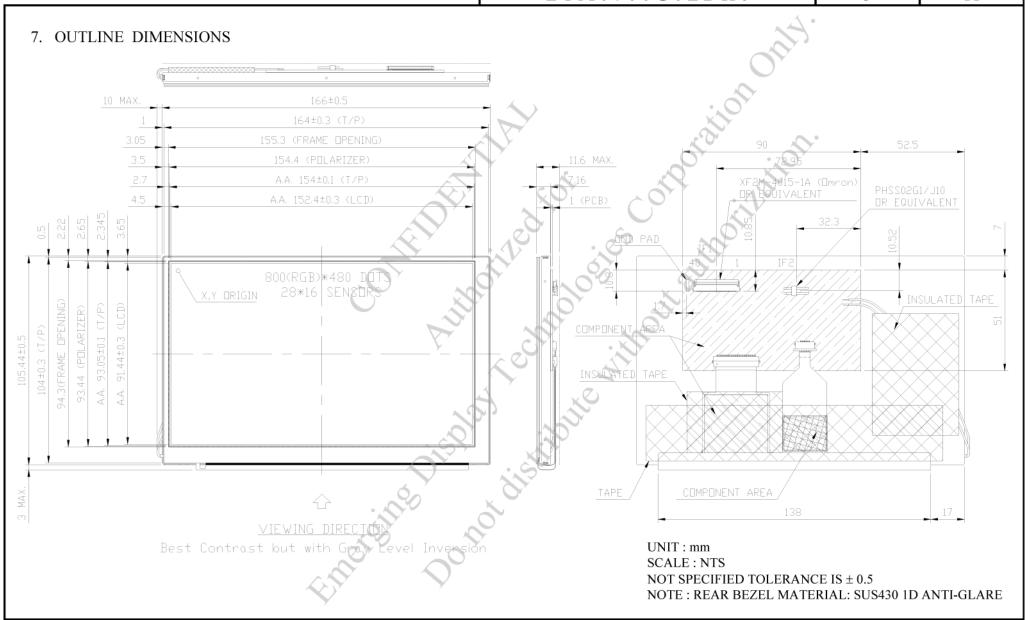
6.3 THE CALCULATING METHOD OF UNIFORMITY

MAXIMUM BRIGHTNESS-MINIMUM BRIGHTNESS UNIFORMITY: 1 AVERAGE BRIGHTNESS

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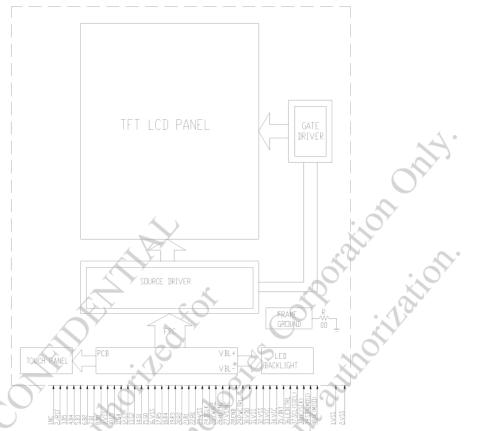
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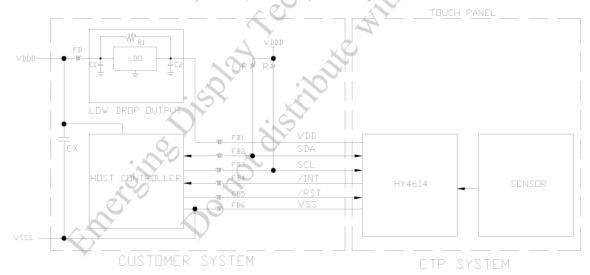
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8. BLOCK DIAGRAM



NOTE : UD = "H" LEVEL : OUT1 \rightarrow 480 ; LR = "H" LEVEL : OUT1 \rightarrow 800(DEFAULT) "L" LEVEL : OUT480 \rightarrow 1 (DEFAULT) "L" LEVEL : OUT800 \rightarrow 1



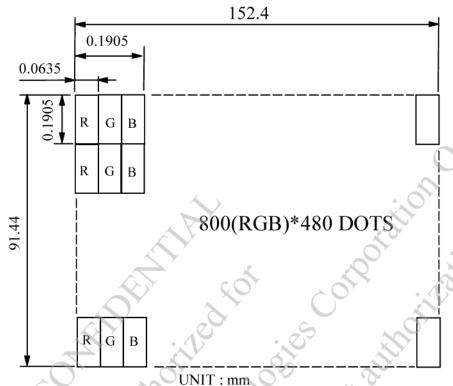
NOTE (1): THE STANDARD IIC COMMUNICATION INTERFACE, SUPREME SCL CLOCK IS 400 KHz, SLAVE ADDRESS CAN BE SET UP, SUPPORTS VDD LEVEL POWER, NEEDS PULL HIGH RESISTANCE AND WE RECOMMEND THE PULL HIGH RESISTANCE IS 2.0K OHM.

NOTE (2) : POWER SUPPLY SHALL BE CLEAN AND NOISE FREE. ADDITIONAL FILTERING OR A SEPARATE LDO (LOW DROP OUT) REGULATOR CAN BE REQUIRED. C1 AND C2 CAPACITORS RECOMMENDATION : $4.7\mu F$ OR $10~\mu F$

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SCALE: NTS
NOT SPECIFIED TOLERANCE IS ± 0.1
DOTS MATRIX TOLERANCE IS ± 0.01

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10. INTERFACE SIGNALS

10.1 IF1:

PIN NO.	SYMBOL	I/O/P	FUNCTION	N			
1	NC	_	NON CONNECTION				
2	/RST	I	EXTERNAL RESET, LOW IS ACTIV	E TOUCH PANEL			
3	В5	I	BLUE DATA BIT 5	4 .			
4	B4	I	BLUE DATA BIT 4				
5	В3	I	BLUE DATA BIT 3				
6	B2	I	BLUE DATA BIT 2				
7	B1	I	BLUE DATA BIT 1				
8	В0	I	BLUE DATA BIT 0				
9	VSS	Р	GROUND (VSS IS CONNECTED TO CONDUCTIVE TAPE)	METAL HOUSING WITH			
10	G5	I	GREEN DATA BIT 5				
11	G4	I	GREEN DATA BIT 4				
12	G3	I	GREEN DATA BIT 3				
13	G2	1	GREEN DATA BIT 2				
14	G1	AL.	GREEN DATA BIT 1				
15	G0	Ĭ	GREEN DATA BIT 0				
16	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)				
17	R5	I	RED DATA BIT 5				
18	R4	I	RED DATA BIT 4				
19	R3	I	RED DATA BIT 3				
20	R2	I	RED DATA BIT 2				
21	R1	I	RED DATA BIT 1				
22	R0	I	RED DATA BIT 0				
23	VSS	Р	GROUND (VSS IS CONNECTED TO CONDUCTIVE TAPE)	METAL HOUSING WITH			
24	DCLK	I	DOT DATA CLOCK				
25	NC	200	NON CONNECTION				
26			HORIZONTAL SYNC INPUT.	DE MODE : HSYNC,			
26	HSYNC	ό I	INTERNALLY PULL HIGH.	VSYNC CONNECTED			
27	VCVAIC	т.С	VERTICAL SYNC INPUT.	TO VDD.			
27	VSYNC	1	INTERNALLY PULL HIGH.	SYNC MODE: ENB			
20	ENID	т	INPUT DATA ENABLE CONTROL.	CONNECTED TO			
28	ENB	I	INTERNALLY PULLED LOW.	GROUND.			

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PIN NO.	SYMBOL	I/O/P	FUNCTION				
			PWCTRL REMARK				
			LOGIC LEVEL H POWER ON				
29	PWCTRL	I	H=3.3V L=0V L SHUTDOWN				
30	VDD	P	POWER SUPPLY VOLTAGE				
31	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH				
			CONDUCTIVE TAPE)				
32	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)				
33	VCC	P	POWER SUPPLY FOR LED DRIVER CIRCUIT				
34	VCC	P	POWER SUPPLY FOR LED DRIVER CIRCUIT				
35	/INT	О	EXTERNAL INTERRUPT TO THE HOST TOUCH PANEL				
36	LEDCTRL	I	BRIGHTNESS CONTROL FOR LED BACKLIGHT				
37	SCL	I	I2C CLOCK INPUT				
38	NC		NON CONNECTION TOUCH PANEL				
39	SDA	I/O	I2C DATA INPUT AND OUTPUT				
40	NC	<u> </u>	NON CONNECTION				

10.2 IF2:

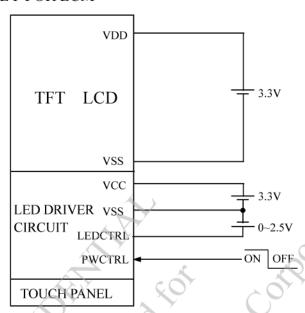
PIN NO.	SYMBOL	I/O/P	FUNCTION
1	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
2	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
	C.M.C.		Sonot distribute

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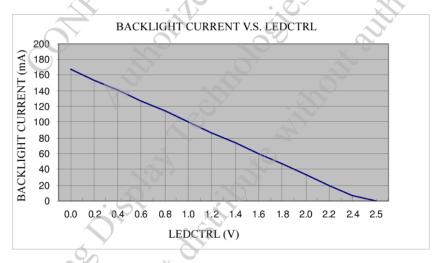
3D

11. POWER SUPPLY

11.1 POWER SUPPLY FOR LCM



11.2 THE BRIGHTNESS CONTROLLED BY BACKLIGHT CURRENT OF LEDCTRL



12. CAPACITIVE TOUCH PANEL SPECIFICATION

12.1 OPTICAL CHARACTERISTICS

ITEM	CONDITION	MIN.	TYP.	MAX.	UNIT
TRANSPARENCY	Ta = 25°C	85			%
NOTE(1)	1a – 23 C	65	_		/0

NOTE (1): OPTICAL MEASUREMENT SHOULD BE EXECUTED AFTER PANEL IS SECURED.

MEASUREMENT PROCESS SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS SHOULD BE MEASURED BY SPECTROPHOTOMETER.

12.2 HARDNESS

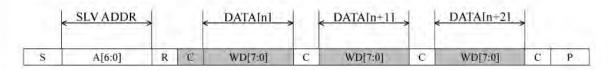
ITEM	DESCRIPTION
SURFACE HARDNESS	7H (MIN.)

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12.3 PROTOCOL

12.3.1 I2C READ

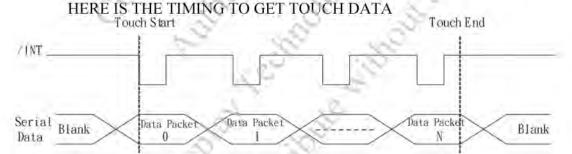


CHARACTER	DESCRIPITION
S	I2C START OR I2C RESTART
A[6:0]	SLAVE ADDRESS, THE VALUE CAN BE CUSTOMIZED
R	OPERATOR BYTE, SHOULD BE 1'b1, STANDS FOR READ
C	ACK SIGNAL
P	STOP SIGNAL (STOP SIGNAL IS OPTIONAL, RESTART SIGNAL IS ALSO OK FOR NEXT PACKET)

SLAVE ADDRESS=0x38

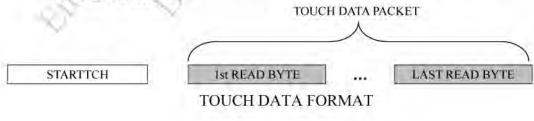
12.3.2 INTERRUPT SIGNAL FOR CTPM TO HOST

AS FOR STANDARD CTPM, HOST NEED TO USE BOTH INTERRUPT CONTROL SIGNAL AND SERIAL DATA INTERFACE TO GET THE TOUCH DATA.



12.3.3 READ TOUCH DATA PACKET

WE DEFINED A CTPM PERIOD AS EACH CAPACITANCE DATA GATHERING AND DATA PROCESS, IN EACH CTPM, IF THERE IS A TOUCH DETECTS, THERE WILL WE A FAME OF TOUCH DATA. HOST CAN GET THE SPECIFIED FORMAT TOUCH DATA BY SERIAL DATA INTERFACE.



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TOUCH DATA READ PROTOCOL

IN THIS MODE THE CTP IS FULLY FUNCTIONAL AS A TOUCH SCREEN CONTROLLER. READ AND WRITE ACCESS ADDRESS IS JUST LOGICAL ADDRESS WHICH IS NOT ENFORCED BY HARDWARE OR FIRMWARE. HERE IS THE OPERATING MODE REGISTER MAP.

ADDRESS	NAME	BIT7 BIT6	BIT5	BIT4	BIT3	BIT2	BIT1	BIT0	HOST ACCESS
02h	TD_STATUS		'		NUMB TOUCI			 	R
03h	TOUCH1_XH	1 st EVENT FLAG			1st TOU				R
04h	TOUCH1_XL	1st TOUCH X	POSITIO	N[7:0					R
05h	TOUCH1_YH	1 st TOUCH ID	[3:0]		1 st TOU Y POSI	CH TION[1	1:8]	0	R
06h	TOUCH1_YL	1st TOUCH Y	POSITIO	N[7:0]			0,	R
07h			7					7	
08h							2.0		⟨ ` .
09h	TOUCH2_XH	2 nd EVENT FLAG	A	2	2 nd TOU X POSI	JCH TION[]	1:8]		R
0Ah	TOUCH2_XL	2 nd TOUCH X	POSITIO)N[7:0)]	0,7	′	. 1	R
0Bh	TOUCH2_YH	2 nd TOUCH II	D[3:0]		2 nd TOU Y POS	JCH TION[1	1:8]		R
0Ch	TOUCH2_YL	2 nd TOUCH Y	POSITIO	ON[7:0)] 🧳		20		R
0Dh	~								R
0Eh	A			0	0	0	2		R
0Fh	TOUCH3_XH	3 rd EVENT FLAG		2	3 rd TOU X POS	ICH TION[1	1:8]		R
10h	TOUCH3_XL	3 rd TOUCH X	POSITIO	N[7:0		0			R
11h	TOUCH3_YH	3 rd TOUCH ID	[3:0]		3 rd TOU Y POSI	ICH TION[1	1:8]		R
12h	TOUCH3 YL	3 rd TOUCH Y	POSITIO	N[7:0	7				R
13h	_	1		10					R
14h		100							R
15h	TOUCH4_XH	4 th EVENT FLAG	100		4 th TOU X POS	JCH TION[1]	1:8]		R
16h	TOUCH4_XL	4 th TOUCH X	POSITIO	N[7:0]				R
17h	TOUCH4_YH	4 th TOUCH ID	0[3:0]		4 th TOU Y POSI	JCH TION[1	1:8]		R
18h	TOUCH4_YL	4 th TOUCH Y	POSITIO	N[7:0]				R
19h	.02	~							R
1Ah									R
1Bh	тоисн5_хн	5 th EVENT FLAG				JCH TION[1	1:8]		R
1Ch	TOUCH5_XL	5 th TOUCH X	POSITIO	N[7:0					R
1Dh	TOUCH5_YH	5 th TOUCH ID				JCH TION[1]	1:8]		R
1Eh	TOUCH5_YL	5 th TOUCH Y	POSITIO	N[7:0					R
1Fh	_								R
20h									R
A6h	ID G FIRMID	FIRMWARE I	ID						R

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TD STATUS

THIS REGISTER IS THE TOUCH DATA STATUS REGISTER.

ADDRESS	BIT ADDRESS	REGISTER NAME	DESCRIPTION
02h	3:0	INLIMBER OF TOLICH POINTS 13:01	HOW MANY POINTS DETECTED. 1-5 IS VALID.
	7:4	NONE	NONE

TOUCHn XH (n:1-5)

THIS REGISTER DESCRIBES MSB OF THE X COORDINATE OF THE NTH TOUCH POINT AND THE CORRESPONDING EVENT FLAG.

ADDRESS	BIT ADDRESS	REGISTER NAME	DESCRIPTION
03h ∼ 1Bh	7:6	EVENT FLAG	00b: PUT DOWN 01b: PUT UP 10b: CONTACT 11b: RESERVED
	5:4	NONE	RESERVED
	3:0	TOUCH X POSITION [11:8]	MSB OF TOUCH X POSITION IN PIXELS

TOUCHn_XL (n:1-5)

THIS REGISTER DESCRIBES LSB OF THE X COORDINATE OF THE NTH TOUCH POINT.

ADDRESS	BIT ADDRESS	REGISTER NAME	DESCRIPTION
$04h \sim 1Ch$	7:0	TOUCH X POSITION 17:01	LSB OF THE TOUCH X POSITION IN PIXELS

TOUCHn YH (n:1-5)

THIS REGISTER DESCRIBES MSB OF THE Y COORDINATE OF THE NTH TOUCH POINT AND CORRESPONDING TOUCH ID.

	JOINTED TO	IDITIO TOCCITIDI		
	ADDRESS	BIT ADDRESS	REGISTER NAME	DESCRIPTION
Γ		7:4	TOUCH ID [3:0]	TOUCH ID OF TOUCH POINT
	$05h \sim 1Dh$	3:0	ITOUCH X POSITION III:81	MSB OF TOUCH Y POSITION IN PIXELS

TOUCHn YL (n:1-5)

THIS REGISTER DESCRIBES LSB OF THE Y COORDINATE OF THE NTH TOUCH POINT.

ADDRESS	BIT ADDRESS	REGISTER NAME	DESCRIPTION
06h ~ 1Eh	7:0	TOUCH X POSITION [7:0]	LSB OF THE TOUCH Y POSITION IN PIXELS

ID_G_FIRMWARE ID

THIS REGISTER DESCRIBES THE FIRMWARE ID OF THE APPLICATION

ADDRESS	BIT ADDRESS	REGISTER NAME	DESCRIPTION
A6h	7:0		FIRMWARE VERSION

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12.4 INSPECTION STANDARDS

INSPECTION ITEMS	CRITERI <i>I</i>	1	REMARK
	THE FOLLOWING BLACK/WH ARE WITHIN THE VIEWING A AVERAGE DIAMETER: D (mn	REA.	
	SIZE D	PERMISSIBLE NO.	
BLACK/WHITE SPOT	D≤0.1mm	IGNORE	
	0.1mm <d≤0.3mm< td=""><td>5</td><td></td></d≤0.3mm<>	5	
	0.3mm <d≤0.5mm< td=""><td>5</td><td>/ A.</td></d≤0.5mm<>	5	/ A.
	D>0.5 mm	0	
	NOTE (1): THE DISTANCE BET SHOULD BE MORE		0'
	THE FOLLOWING BLACK LIN LINE IS WITHIN THE VIEWING	,	101
	WIDTH: W (mm), LENGH: L ((mm)	← L →
SCRATCH	SIZE W & L	PERMISSIBLE NO.	
SCRATCH	W≤0.05mm	IGNORE	
	0.05mm <w≤0.07mm, l≤5mm<="" td=""><td>10</td><td> W</td></w≤0.07mm,>	10	W
	W>0.07mm	0	O.C.
LINEAR TYPE / FOREIGN FIBER	THE FOLLOWING BLACK LIN LINE IS WITHIN THE VIEWING WIDTH: W (mm), LENGH: L (SIZE W & L W≤0.05mm 0.05mm <w≤0.07mm, l≤5mm="" w="">0.07mm</w≤0.07mm,>	G AREA.	L L
	BUBBLES WITHIN VIEWING A	REA.	
	AVERAGE DIAMETER: D (mn		
	SIZE D	PERMISSIBLE NO.	~ /
BUBBLE / DENT	D≤0.2mm	IGNORE	Ø
DODDLE / DENT	0.2mm <d≤0.3mm 0.3mm<d≤0.5mm< td=""><td>3</td><td>T</td></d≤0.5mm<></d≤0.3mm 	3	T
	D>0.5mm	0	
CHIP DAMAGE ON	I I CORNER I	$Y \le 3mm \cdot Z \le t$ HICKNESS)	Chip of glass
GLASS	EDGE X ≤ 6mm	$Y \le 1 mm$, $Z \le t$ HICKNESS)	I V V V V V V V V V V V V V V V V V V V

NOTE:

- 1.FOR ANY SPOTS OR LINES, WHICH ARE NOT OBSERVED UNDER APPROPRIATE PANEL OPERATING CONDITION ARE DEEMED ACCEPTABLE.
- 2.THE FOREIGN MATERIALS THAT CAN BE BLOWN OUT BY AIR AND REMOVED BY WET CLEANING ARE NOT REGARDED AS DEFECTS.

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13. INSPECTION CRITERION

13.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.(E.D.T) TO CUSTOMERS

13.2 INSPECTION CONDITIONS

13.2.1 (1)OBSERVATION DISTANCE: 35±5cm

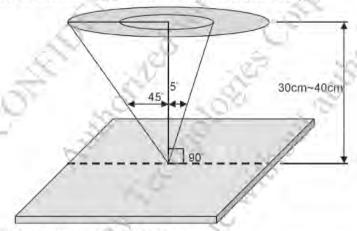
(2) VIEW ANGLE:

NON-OPERATION CONDITION: ±5°

(PERPENDICULAR TO LCD PANEL SURFACE)

OPERATION CONDITION: ±45°

(PERPENDICULAR TO LCD PANEL SURFACE)



13.2.2 ENVIRONMENT CONDITIONS:

AMBIE	25±5°C	
AMBIENT HUMIDITY		65±20%RH
AMBIENT	COSMETIC INSPECTION	MORE THAN 600Lux
ILLUMINATION FUNCTIONAL INSPECTION		300~500 Lux

13.2.3 INSPECTION LOT QUANTITY PER DELIVERY LOT FOR EACH MODEL

13.2.4 INSPECTION METHOD

A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY

(a)APPLICABLE STANDARD: MIL-STD-105E NORMAL INSPECTION, SINGLE SAMPLING LEVEL II

(b)AQL: MAJOR DEFECT: AQL 0.65 MINOR DEFECT: AQL 1.0

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13.3 INSPECTION STANDARDS

13.3.1 VISUAL DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL	
	1.DISPLAY ON	DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC		
MAJOR DEFECT	2.BACKLIGHT	NO LIGHT FLICKERING AND OTHER ABNORMAL ILLUMINATION	0.65	
	3.DIMENSIONS	SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	>.	
MINOR DEFECT	1.DISPLAY ZONE 2.BEZEL ZONE 3.SOLDERING 4.DISPLAY ON (ALL ON)	 BLACK/WHITE SPOT BUBBLES ON POLARIZER NEWTON RING BLACK/WHITE LINE SCRATCH CONTAMINATION LEVER COLOR SPREED STAINS SCRATCHES FOREIGN MATTER INSUFFICIENT SOLDER SOLDERED IN INCORRECT POSITION CONVEX SOLDERING SPOT SOLDER BALLS SOLDER SCRAPS LIGHT LINE 	1.0	

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13.3.2 MODULE DEFECTS CLASSIFICATION

NO.	ITEM	CRITI	ERIA
1	DISPLAY ON INSPECTION	(1)INCORRECT PATTERN (2)MISSING SEGMENT (3)DIM SEGMENT (4)OPERATING VOLTAGE BEYOND S	SPEC
2	OVERALL DIMENSIONS	(1)OVERALL DIMENSION BEYOND S	SPEC
3	DOT DEFECT	(1)INSPECTION PATTERN: FULL WH AND BLUE SCREENS. (2) ITEMS BRIGHT DOT DARK DOT TOTAL BRIGHT AND DARK DOTS NOTE: 1. THE DEFINITION OF DOT: THE SIZE OF A DEFECTIVE DOT OREGARDED AS ONE DEFECTIVE E 2. BRIGHT DOT: DOTS APPEAR BRIGHT AND UNCLED BRIGHT SOLUTION OF BL 3. DARK DOT: DOTS APPEAR DARK AND UNCHAR PANEL IS DISPLAYING UNDER PU	ACCEPTABLE COUNT N ≤ 4 N ≤ 5 N ≤ 5 OVER 1/2 OF WHOLE DOT IS OOT. HANGED IN SIZE IN WHICH LCD ANGED IN SIZE IN WHICH LCD
4	FOREIGN BLACK/WHITE/ BRIGHT LINE/ SCRATCH OF VIEWING AREA		PERMISSIBLE NO. IGNORE 4 NONE
5	FOREIGN MATTER \ BLACK SPOTS \ WHITE SPOTS \ DENT (INCLUDING LIGHT LEAKAGE DUE TO POLARIZING PLATES PINHOLES, ETC.)	AVERAGE DIAMETER (mm): D N $D \le 0.15$ $0.15 < D \le 0.5$ $0.5 < D$ NOTE : DIAMETER D=(a+b)/2	IUMBER OF PIECES PERMITTED IGNORE 4 NONE

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NO.	ITEM	CRITERIA			
			AVERAGE DIAMETER (mm): D	NUMBER OF PIECES PERMITTED	
		DUDDI E ON THE	D ≤ 0.25	IGNORE	
		BUBBLE ON THE POLARIZER	$0.25 < D \le 0.5$	N ≤ 5	
		TOLARIZER	0.5 < D	NONE	
		SURFACE STAINS	D < 0.1	IGNORE	
		SCHARCE STAINS	$0.1 < D \le 0.3$	N ≤ 3	
		CF FAIL / SPOT	D < 0.1	IGNORE	
			$0.1 < D \le 0.3$	N ≤ 3	
6	BUBBLES OF POLARIZER /DIRT/CF FAIL /SURFACE STAINS NOTE: (1)POLARIZER BUBBLE IS DEFINED AS THE BUBBLE APPEARS ON ACTIVE DISPLAY AREA. THE DEFECT OF POLARIZER BUBBLE SHALL BE IGNORED IF THE POLARIZER BUBBLE APPEARS ON THE OUTSIDE OF ACTIVE DISPLAY AREA. (2)THE EXTRANEOUS SUBSTANCE IS DEFINED AS IT CAN BE OBSERVED WHEN THE MODULE IS POWER ON. (3)THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINED AS FOLLOWING. AVERAGE DIAMETER (D)=(a+b)/2				
7	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOWED		
8	MURA ON DISPLAY	IT'S OK IF MURA IS SLIGHT VISIBLE THROUGH 6% ND FILTER			
9	UNEVEN COLOR SPREAD, COLORATION	(1)TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.			
10	BEZEL APPEARANCE	(1)BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. (2)BEZEL MUST COMPLY WITH JOB SPECIFICATIONS.			
11	РСВ	THE SEAL AREA (THAN THREE PLA (2)NO OXIDATION O (3)PARTS ON PCB M CHARACTERISTIC THERE SHOULD B PARTS. (4)THE JUMPER ON CHARACTERISTIC (5)IF SOLDER GETS	OR CONTAMINATION PCE UST BE THE SAME AS OF CCHART. SE NO WRONG PARTS, M THE PCB SHOULD CONFO	E SHOULD BE NO MORE B TERMINALS. N THE PRODUCTION ISSING PARTS OR EXCES ORM TO THE PRODUCT ED PAD, ZEBRA PAD OR	

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NO	ITEM	CRITERIA
NO.	SOLDERING	CRITERIA (1)NO SOLDERING FOUND ON THE SPECIFIED PLACE (2)INSUFFICENT SOLDER (a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD SOLDER FILLET • SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING SOLDER FILLET • SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED SOLDER (3)PARTS ALIGMENT (a)LSI, IC LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE

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NO.	ITEM	CRITERIA
		(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE
12	SOLDERING	
		 (4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. (5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. (6)NO RESIDUE OR SOLDER BALLS ON PCB. (7)NO SHORT CIRCUITS IN COMPONENTS ON PCB.
13	BACKLIGHT	(1)NO LIGHT (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
14	GENERAL APPEARANCE	 (1)NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. (2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP. (3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. (4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. (5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. (6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. (7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. (8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. (9)LCD PIN LOOSE OR MISSING PINS. (10)PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. (11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. (12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.

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ITEM CRITERIA NO. THE LCD WITH EXTENSIVE CRACK IS NOT ACCEPTABLE GENERAL GLASS CHIP: < VIEWING AREA ≤ 1/8X ≤ t/2 ≤ W/2 ≤ 1/8X $t/2 > , \le 2t$ *W=DISTANCE BETWEEN SEALANT AREA AND LCD PANEL EDGE X = LCD SIDE LENGTH t = GLASS THICKNESS ORNER PART < VIEWING AREA ≤ 1/8X $\leq t/2$ > t/2 , ≤ 2t ≤ 1/8X *W=DISTANCE BETWEEN SEALANT AREA AND LCD CRACKED GLAS 15 PANEL EDGE X = LCD SIDE LENGTH t = GLASS THICKNESS b CHIP ON ELECTRODE PAD $\leq 0.5 mm$ $\leq 1/8X$ X=LCD SIDE WIDTH t =GLASS THICKNESS ≤ 1/8X ≤L $\leq t$ X=LCD SIDE WIDTH t = GLASS THICKNESS L=ELECTRODE PAD LENGTH **OIF GLASS CHIPPING THE ITO** TERMINAL, OVER 2/3 OF THE ITO MUST REMAIN AND BE, INSPECTED ACCORDING TO ELECTRODE TERMINAL **SPECIFICATIONS** ②IF THE PRODUCT WILL BE HEAT SEALED BY THE CUSTOMER, THE ALIGNMENT MARK MUST NOT BE DEMAGED

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13.4 RELIABILITY TEST

13.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO.	ITEM	DESCRIPTION
1	HIGH TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR 240 HRS
2	LOW TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR 240 HRS
3	HIGH TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°C FOR 240 HRS
4	LOW TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS
5	HIGH TEMPERATURE /HUMIDITY TEST STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C, 90% RH 240 HRS
6	THERMAL SHOCK (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION: +80°C -30°C -30°C 30 min 30 min 1CYCLE
7	ESD (ELECTROSTATIC DISCHARGE) (NOT OPERATED)	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV (ACCORDING TO IEC-61000-4-2)

NOTE (1): THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

13.5 TESTING CONDITIONS AND INSPECTION CRITERIA

FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM TEMPERATURE FOR 24 HOURS, AFTER THE TESTS LISTED IN TABLE 13.5, STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN ORDER TO ENSURE STABILITY.

NO.	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION	I DEFED TO SPECIFIC ATTOM	THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	I DEFED IN SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

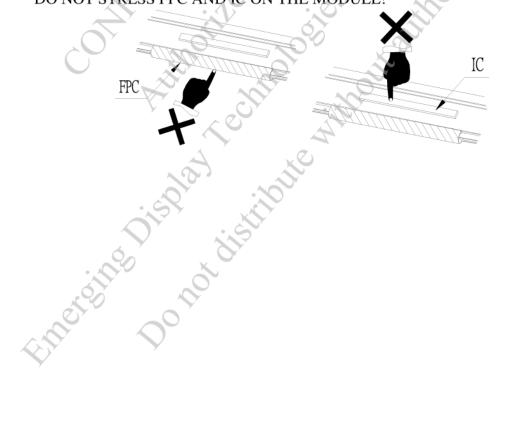
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13.6 OPERATION

- 13.6.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 13.6.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR. WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY.
- 13.6.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST .
- 13.6.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE.

 IF ABOVE SEQUENCE IS NOT FOLLOWED, CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH UP PROBLEM.
- 13.6.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!

 DO NOT STRESS FPC AND IC ON THE MODULE!



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13.7 NOTICE

- 13.7.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS. FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD.
- 13.7.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED.
- 13.7.3 DO NOT CHARGE STATIC ELECTRICITY, AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC-PROTECTED MATERIAL.
- 13.7.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE.
- 13.7.5 DON'T GIVE EXTERNAL SHOCK.
- 13.7.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 13.7.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.
 WHEN THE LIQUID IS ATTACHED TO YOUR, SKIN, CLOTH ETC. WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 13.7.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 13.7.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS AND SOLVENT.
- 13.7.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 13.7.11 REWIRING: NO MORE THAN 3 TIMES.