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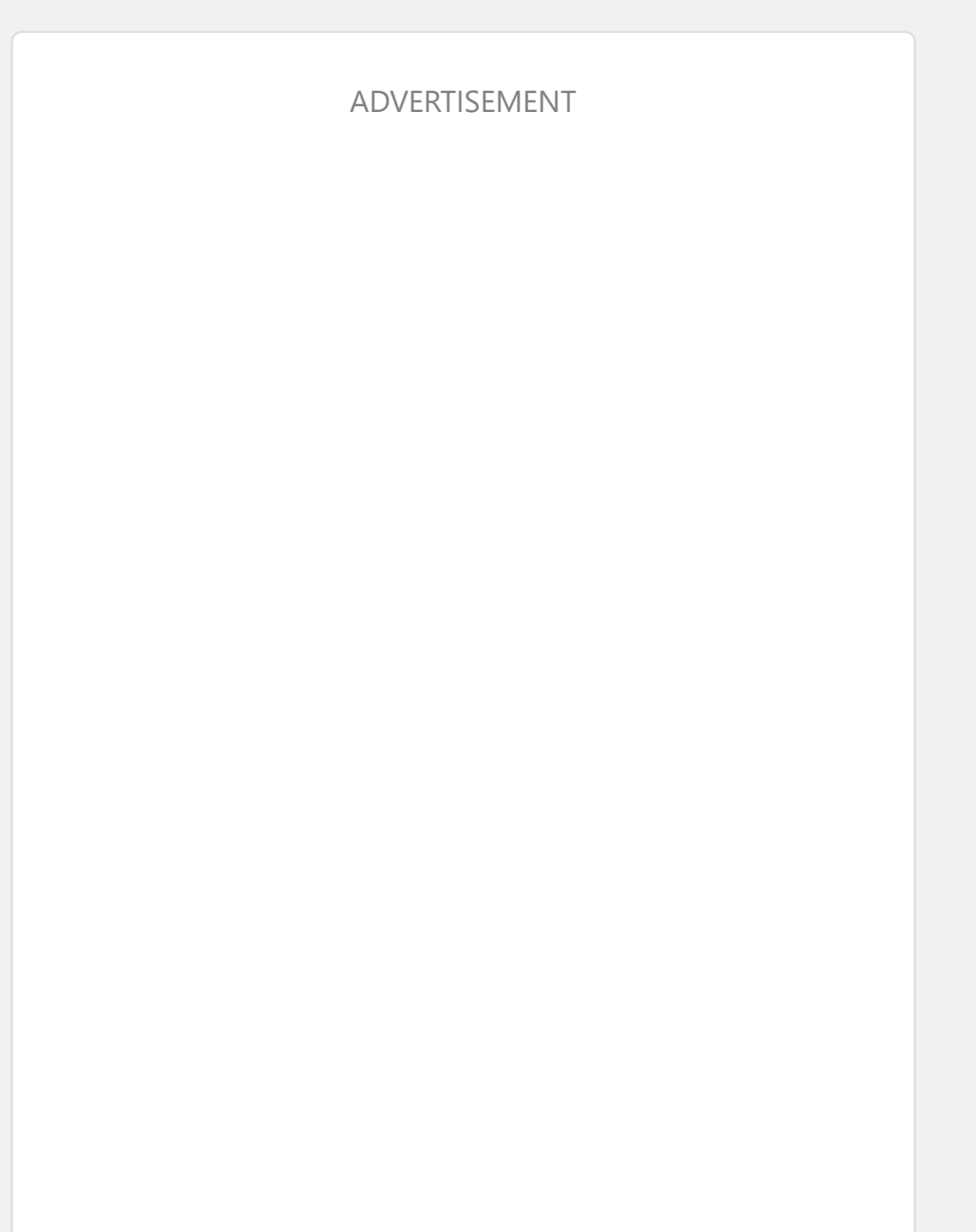
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- 8-Bit Serial-In, Parallel-Out Shift
- Wide Operating Voltage Range of 2 V to 6 V
- High-Current 3-State Outputs Can Drive Up To 15 LSTTL Loads
- Low Power Consumption, 80- μ A Max I_{CC}
- Typical $t_{pd} = 13$ ns
- ± 6 -mA Output Drive at 5 V
- Low Input Current of 1 μ A Max
- Shift Register Has Direct Clear

SN54HC595, SN74HC595
8-BIT SHIFT REGISTERS
WITH 3-STATE OUTPUT REGISTERS
SCLS041G - DECEMBER 1982 - REVISED FEBRUARY 2004

SN54HC595 . . . J OR W PACKAGE
 SN74HC595 . . . D, DB, DW, N, OR NS PACKAGE
 (TOP VIEW)

Q _B	1	16	V _{CC}
Q _C	2	15	Q _A
Q _D	3	14	SER
Q _E	4	13	\overline{OE}
Q _F	5	12	RCLK
Q _G	6	11	SRCLK
Q _H	7	10	SRCLR
GND	8	9	Q _A

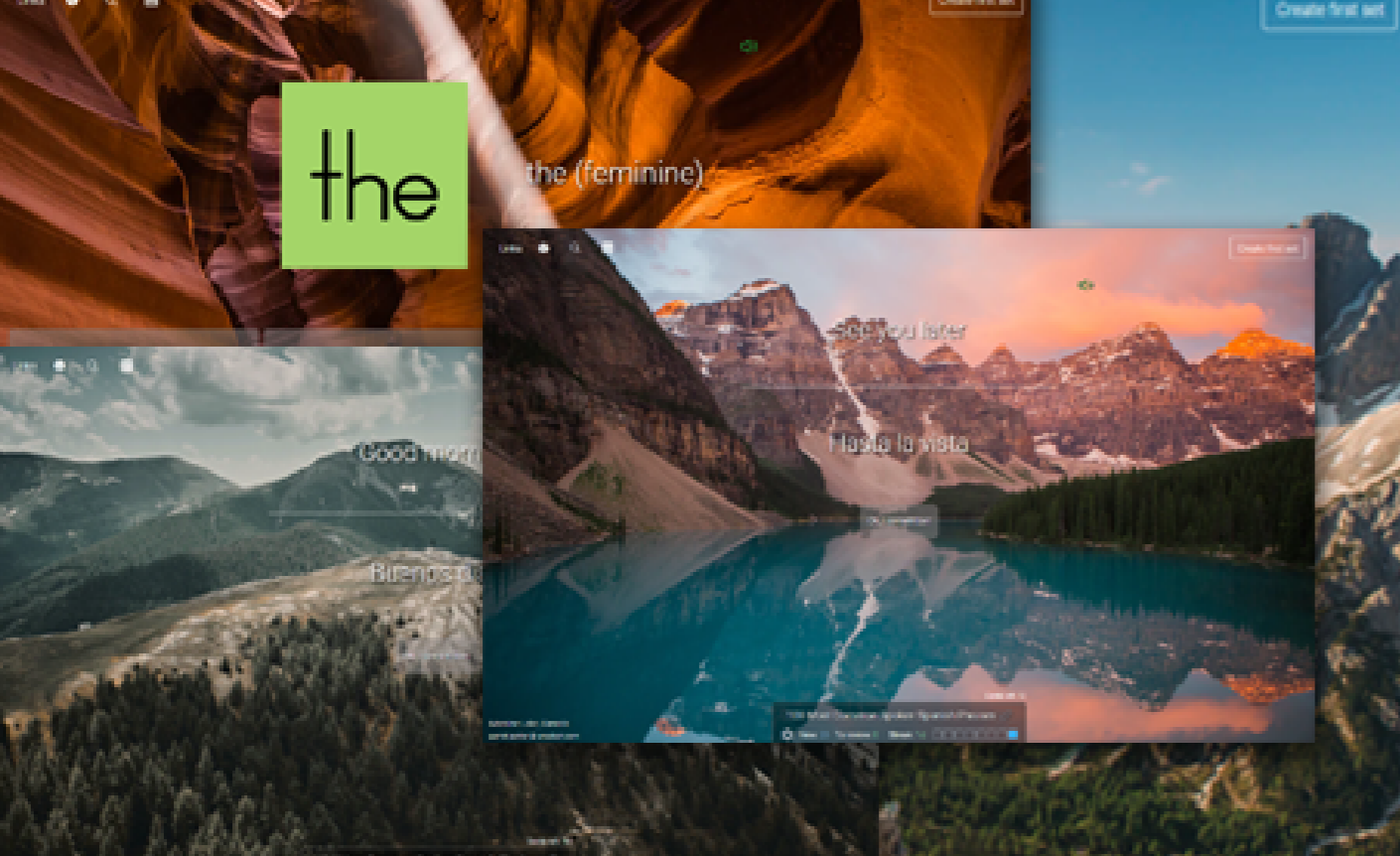


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